Concurrent Timestamping Made Simple

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Abstract

Concurrent Time-stamp Systems (CTSS) allow processes to temporally order concurrent events in an asynchronous shared memory system, a powerful tool for concurrency control, serving as the basis for solutions to coordination problems such as mutual exclusion, $\ell$-exclusion, randomized consensus, and multi-writer multi-reader atomic registers. Solutions to these problems all use an “unbounded number” based concurrent time-stamp system (UCTSS), a construction which is as simple to use as it is to understand. A bounded “black-box” replacement of UCTSS would imply equally simple bounded solutions to most of these extensively researched problems. Unfortunately, while all known applications use UCTSS, all existing solution algorithms are only proven to implement the Dolev-Shavit CTSS axioms, which have been widely criticized as “hard-to-use.” While it is easy to show that a UCTSS implements the CTSS axioms, there is no proof that a system meeting the CTSS axioms implements UCTSS. Thus, the problem of constructing a bounded black-box replacement for uctss remains open.

This paper presents the first such bounded black-box replacement of uctss. The key to the solution is a simplified variant of the Dolev-Shavit CTSS algorithm based on the atomic snapshot object proposed by Afek et. al. and Anderson, in a way that limits the number of interleavings that can occur, and whose behaviours can be readily mapped to those of UCTSS. Using the forward simulation techniques of the I/O Automata model, we are then able show that our bounded algorithm behaves like UCTSS. The forward simulation allows us to present, what would otherwise be a complicated proof, as an extensive, yet at each step simple case analysis. In fact, we believe that large parts of the forward simulation proof can be checked using an automatic proof checker such as Larch.

For read/write memory, our easy to use bounded UCTSS is only a logarithmic factor from the most efficient known bounded CTSS constructions. Moreover, unlike these efficient algorithms, our modular use of an atomic snapshot object implies that our constructions are not limited to read/write memory, and can be applied in any computation model whose basic operations suffice to provide a wait-free snapshot implementation. The complexity of our bounded UCTSS will be the same as the complexity of the underlaying snapshot implementation used.

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1 Introduction

A timestamp system is somewhat like a ticket machine at an ice cream parlor. People’s requests to buy the ice cream are timestamped based on a numbered ticket (label) taken from the machine. Any person, in order to know in what order the requests will be served, need only scan through all the numbers and observe the order among them. A concurrent timestamp system (CTSS) is a timestamp system in which any process can either take a new ticket or scan the existing tickets simultaneously with other processes. A CTSS is required to be waitfree, which means that a process is guaranteed to finish any of the two above mentioned label-taking or scanning tasks in a finite number of steps, even if other processes experience stopping failures. Waitfree algorithms are highly suited for fault tolerant and realtime applications (Herlihy, see [13]).

The paradigm of concurrent timestamping is at the heart of solutions to some of the most fundamental problems in multiprocessor concurrency control. Examples of such algorithms include Lamport’s first come first served mutual exclusion [17], Vitanyi and Awerbuch’s construction of a multi-reader multi-writer atomic register [25], Abrahamson’s randomized consensus [1], and Afek, Dolev, Gafni, Merritt, and Shavit’s first come first enabled t-exclusion [3]. Solutions to these problems all use an “unbounded number” based concurrent timestamp system (UCTSS), a construction which is as simple to use as it is to understand. A bounded “black-box” replacement of UCTSS would imply equally simple bounded solutions to these extensively researched problems.

1.1 Related Research

Israeli and Li, in [15], were the first to isolate the notion of bounded timestamping (timestamping using bounded size memory) as an independent concept, developing an elegant theory of bounded sequential timestamp systems. Sequential timestamp systems prohibit concurrent operations. This work was continued in several interesting papers on sequential systems with weaker ordering requirements by Li and Vitanyi [21], Cori and Sopena [8] and Saks and Zeharoglou [26]. Dolev and Shavit [9] were the first to provide an axiomatic definition and a construction of a bounded concurrent timestamp system using read/write registers. Because of the importance of the bounded concurrent timestamping problem, the original solution by Dolev and Shavit has been followed by a series of papers directed at providing more efficient and simple to understand bounded CTSS algorithms. Israeli and Pinchasov [16] have simplified the [9] algorithm by modifying the labeling scheme of [9], introducing a new label scanning method, and replacing the ordering-of-events based formal proof [18] of the CTSS axioms. Concurrent with our work, Dwork and Waarts [10] presented the most efficient read/write register based CTSS construction to date, taking only $O(n)$ time for either a scan or update. They model their bounded construction after a new type of unbounded CTSS construction, where processes choose from “local pools” of label values instead of the simple “global pool” based UCTSS [1, 3, 17, 25]. In order to bound the number of possible label values in the local pool of the bounded implementation, they introduce a form of amortized garbage collection. They then prove that the linear time bounded implementation meets the CTSS axioms of [9]. In [11], Dwork, Herlihy, Plotkin, and Waarts introduce an alternative linear complexity bounded CTSS construction that combines a time-lapse snapshot with the bounded CTSS algorithm of [9]. The proof of their algorithm leverages the axiomatic proof in [9] by arguing that the executions of
their algorithm are a subset of the executions of the algorithm in [11].

We observe that all known applications use uCTSS, but all existing bounded solutions are only proven to implement the CTSS axioms, which have been widely criticized as “hard-to-use.” It is easy to show that a uCTSS implements the CTSS axioms, yet there is no proof that a system meeting the CTSS axioms implements uCTSS. Thus, the problem of constructing a bounded black-box replacement for uctss remains open.

1.2 Our Results

This paper presents the first bounded concurrent time-stamp system algorithm (BCTSS) that provably implements uCTSS. The key to the solution is an algorithm whose behaviours can be readily mapped to those of uCTSS, and the use of a forward simulation proof technique. Our solution consists of the following steps.

- Our algorithm is a variation on the Dolev-Shavit algorithm [9] based on the use of the atomic snapshot primitive introduced by Afek et. al [2] and Anderson [6]. A snapshot primitive allows a process $P_i$ to update the $i$th memory location, or snap the memory, that is, collect an “instantaneous” view of all $n$ shared memory locations. By using a snapshot primitive, we limit the number of interleavings that can occur, and are able to introduce a much simplified version of the labeling algorithm of [9] that is tailored so as to allow a forward simulation proof [22]. Moreover, as we elaborate later, our algorithm is no longer limited to read/write memory, and holds in any computation model whose basic operations suffice to provide a wait-free snapshot implementation.

- Our proof that the bounded algorithm satisfies the uCTSS specification requires a different technique than any of the operational proofs of the CTSS axioms as found in the literature. We begin by introducing a uCTSS specification that uses, instead of integers, label values taken from the unbounded positive reals. This system is broader than (i.e. strictly includes) the integer based uCTSS used in actual applications, yet surprisingly allows for a simpler proof. We then use the forward simulation techniques of the I/O Automata model of Lynch and Tuttle [22], to show that our bounded algorithm implements the real-number based uCTSS specification. (See [23] for references and a discussion of forward simulation techniques.) The forward simulation techniques allow us to present, what would otherwise be a complicated proof, as an extensive, yet at each step simple case analysis. In fact, we believe that large parts of the forward simulation proof can be checked using an automatic proof checker such as Larch [27, 12].

As mentioned, our algorithm provides a wait-free solution in whatever computation model the atomic snapshot object [2] is implemented, be it single-writer multi-reader registers [4], multi-reader multi-writer registers [14], consensus objects [7], or memory with hardware supported compare-and-swap and fetch-and-add primitives.

The time complexity of our uCTSS construction is simply the complexity of the underlying atomic snapshot implementation. For single-writer multi-reader memory, this is the intricate Attiya and Rachman algorithm [4], which takes $O(n \log n)$ operations for either a scan or an update of memory.
Hence the complexity of our algorithm is $O(n \log n)$ for each operation, a logarithmic factor away from the best know constructions [10, 11, 16].

One can also use snapshot implementations in other computation models: the snapshot algorithm of Inoue, Chen, Masuzawa and Tokura [14] to get an $O(n)$ per operation bounded $\text{ctss}$ using multi-reader multi-writer registers, the Chandra and Dwork [7] algorithm to get $O(1)$ label and $O(n)$ scan operations using consensus or randomized consensus objects, or the Rinyi, Shavit and Touitou [24] algorithm to get $O(1)$ label and $O(n)$ scan operations using memory with hardware supported compare-and-swap and fetch-and-add primitives. Note that the most efficient known shared-memory ctss algorithms [10, 11, 16] do not readily imply efficient algorithms in other computation models. Moreover, stronger computational models do not seem to immediately imply significant simplifications of bounded ctss algorithms.

The paper is organized as follows. Section 2 presents the I/O Automaton model. Our unbounded ctss is introduced in Section 3, and the bounded ctss is introduced in Section 4. Section 5 introduces several key invariants that are needed for the simulation proof of Section 6. Some of the invariant proofs are postponed until Section 7.

2 The I/O Automata Model

We present our algorithm in the context of the I/O Automata model. This model, introduced by Lynch and Tuttle [22], represents algorithms as I/O Automata which are characterized by states, initial states, a set of actions called an action signature, state transitions called steps and an equivalence relation on some of the actions of the action signature called a partition. For a I/O Automaton $A$ its five components are denoted by states($A$), start($A$), sig($A$), steps($A$), and part($A$) respectively.

A step that results from an action is denoted by $(s, \pi, s')$ where $s$ is the original state, $\pi$ is the action, and $s'$ is the new state. If an action can be executed in a state $s$, it is said to be enabled in $s$. If an action is not enabled in state $s$, it is said to be disabled in $s$. Actions are classified into external actions, ext($A$), those visible to the user of the algorithm, and internal actions, int($A$), which are not visible to the user. External actions are further classified into input actions, in($A$), which are under the control of the user of the algorithm, and output actions, out($A$), which are under the control of the algorithm. By definition input actions are enabled in all states. For an I/O Automaton $A$ the tuple consisting of in($A$) and out($A$) is called $A$'s external action signature, extsig($A$). We now give a more precise definition for some of the elements of an I/O Automaton. Specifically, for an I/O Automaton $A$, sig($A$) = (in($A$), out($A$), int($A$)). Furthermore, part($A$) defines an equivalence relation on the set of internal actions and output actions of $A$. Finally, we define acts($A$) = in($A$) $\cup$ out($A$) $\cup$ int($A$).

An execution of an I/O Automaton is an alternating sequence of states and actions that could be produced if the algorithm is executed starting from an initial state. A state is called reachable is it is the final state of some execution. A fair execution, $\alpha$, of infinite length is one in which for all $C \in$ part($A$), if some action from $C$ (not necessarily always the same action) is continuously enabled, $\alpha$ contains infinitely many actions from $C$. A fair execution of finite length is one in which for all $C \in$ part($A$) no actions of $C$ are enabled in the final state. A schedule, sched($\alpha$), is the
projection of an execution $\alpha$ onto the actions of the I/O Automaton. A fair schedule, $\text{fairsched}(\alpha)$, is the projection of a fair execution $\alpha$ on the actions of the I/O Automaton. A behavior, $\text{beh}(\alpha)$, is the projection of an execution $\alpha$ onto the external actions of the I/O Automaton. A fair behavior, $\text{fairbeh}(\alpha)$, is the projection of a fair execution $\alpha$ on the external actions of the I/O Automaton. The set of all possible behaviors of an I/O Automaton $A$ is called $\text{behs}(A)$. The set of all possible fair behaviors of an I/O Automaton $A$ is called $\text{fairbehs}(A)$.

We say that an I/O Automaton $A$ implements another I/O Automaton $B$ if the $\text{fairbehs}(A) \subseteq \text{fairbehs}(B)$. Our correctness proof uses the following theorem on simulation proofs which is a restricted version of a theorem in [22].

**Theorem 2.1** Let $A$ and $B$ be I/O Automata with $\text{sig}(A) = \text{sig}(B)$, $\text{part}(A) = \text{part}(B)$, and $\mathcal{R}$ a relation over the states of $A$ and $B$. Suppose:

1. If $a$ is an initial state of $A$, then there exists an initial state $b$ of $B$ such that $(a, b) \in \mathcal{R}$.

2. Suppose $a$ is a reachable state of $A$ and $b$ is a reachable state of $B$ such that $(a, b) \in \mathcal{R}$. If $(a, \pi, a')$ is a step of $A$ then there exists a state $b'$ of $B$ such that $(b, \pi, b')$ is a step of $B$ and $(a', b') \in \mathcal{R}$.

3. If action $\pi$ is enabled in state $b$ of $B$ and $(a, b) \in \mathcal{R}$ then action $\pi$ is enabled in state $a$ of $A$.

Then $\text{fairbehs}(A) \subseteq \text{fairbehs}(B)$.

In the I/O Automaton model, actions provide the basic communications mechanism. CTSS algorithms, as described in the introduction, communicate using shared registers. We can encode register-based communication in the I/O Automaton model, with allowable operations ranging from single-writer multi-reader to powerful read-modify-write, by encoding the register values in the state of the Automaton. Actions are then used to modify the state.

### 3 An Unbounded Concurrent Timestamp System

This section introduces our unbounded implementation of a concurrent timestamp system, $\text{ucTSS}$. It is a generalization of the traditional unbounded number $\text{uctss}$. In particular, it uses timestamps from $\mathbb{R}^+$ instead of the natural numbers. The code for the operations of $\text{uctss}$ is presented in two forms. Figure 1 uses pseudocode. Figure 2 presents the code in the precondition-effect notation commonly used to describe I/O Automata\(^1\). We use the precondition-effect notation as the basis for our definitions and correctness proof and include the compact and intuitive pseudocode for clarity.

The system models $n$ processes indexed by $\{1 \ldots n\}$. Each process $p_i$ in $\text{uctss}$ can perform a $\text{scan}_i$ and $\text{label}_i$ operation. A $\text{label}_i$ operation allows process $p_i$ to associate a label (timestamp) with a given value. A $\text{scan}_i$ operation allows process $p_i$ to determine the order among values based on their associated labels. The function $\text{newlabel}_i$, which is used by $\text{label}_i$, is defined in Figure 3. This function actually picks the new label. In particular, it non-deterministically picks any real value.

\(^1\text{BCTSS}$ is the name for our bounded implementation. The name is included in the caption since the code in the figure is shared by $\text{BCTSS}$ and $\text{uctss}$. $\text{BCTSS}$ is introduced in Section 4.
number bigger than the largest current label. (Note that the traditional uctss implementations pick $X = 1$ or $X$ as a non-zero natural number.) To determine the largest current label, we use the SNAP_i operation. The SNAP_i operation, defined by Afek et al. [2] and Anderson [6], atomically reads an array of single writer multireader locations. Here we use it to atomically read the current labels for the NEWLABEL_i function. To write the new label determined by the NEWLABEL_i function we use the UPDATE_i operation. A UPDATE_i operation, also defined by [2], writes a value to a single location in the array of single writer multireader location read by SNAP_i. SNAP_i and UPDATE_i are waitfree, therefore their use does not compromise the waitfree properties of our timestamp algorithm. A rigorous theoretical foundation for this claim can be found in [?].

The state of uctss is defined by the shared state and the local state of each of the $n$ processes. The shared and local state of each process, along with the initial values are defined in Figure 2. The state of uctss also has derived variables $t_{max}$ and $i_{max}, t_{max} = \max(t_1 \ldots t_n)$ and $i_{max}$ is the largest process index $i$ such that $t_i = t_{max}$.

In terms of the I/O Automata model, uctss has input actions BEGINLABEL_i(val_i) and BEGINSCAN_i for $i \in \{1 \ldots n\}$. The output actions are ENDLABEL_i and ENDSCAN_i(\vec{\alpha}_i, \vec{\beta}_i). The internal actions of $p_i$ are SNAP_i(\vec{\alpha}_i, \vec{\beta}_i) and UPDATE_i((t_i, v_i), (nt_i, val_i)). The set steps($p_i$) is characterized by the precondition clause in each action. The set part($p_i$) consists of a single equivalence classes $C_i$ where the elements of $C_i$ are the actions SNAP_i(\vec{\alpha}_i, \vec{\beta}_i), ENDSCAN_i(\vec{\alpha}_i, \vec{\beta}_i), UPDATE_i((t_i, v_i), (nt_i, val_i))$, and ENDLABEL_i. The set states($p_i$) is the set of all possible states of $p_i$ where each state is defined by the values of the variables of the shared and local state. The set start($p_i$) is the set consisting of the state defined by the initial values of the variables of the shared and local state.

The shared state is accessed only using the atomic SNAP_i and the UPDATE_i actions. Since SNAP_i and UPDATE_i are atomic, each action of uctss is atomic. Notice that the SNAP_i action makes references to the elements of the vector $\vec{\alpha}_i$ indirectly through the use of $i_{max}$ and $t_{max}$ and in order to calculate $\vec{\alpha}_i$. Since SNAP_i is atomic, the labels in $\vec{\alpha}_i$ are the same as the corresponding labels in the shared state. In other words, $t_i = t_j$ during the action. Consequently, we refer directly to the shared variables $i_{max}, t_{max}$, and $t_i$ rather than their copies $\hat{i}_{max}, \hat{t}_{max},$ and $\hat{t}_i$, when analyzing the SNAP_i action. We note here that we are not concerned with the implementation details of the atomic SNAP_i and UPDATE_i actions. We use them has a black box.
**Shared State:**

$t_i$: The current label associated with process $p_i$; initially 0.

$v_i$: The current value associated with process $p_i$; initially $v_o$.

**Local State:**

$nt_i$: The new label for $p_i$ determined by function `makelabel_i`; initially 0.

$val_i$: The new value for $p_i$ passed to `LABEL_i`; initially $v_o$.

$\tilde{\ell}_i$: An array of labels returned by `SNAP_i`; initially (0...0).

$\tilde{\sigma}_i$: An array of values returned by `SNAP_i`; initially $(v_o, \ldots, v_o)$.

$\tilde{\sigma}_i$: An array of process indexes ordered based on the $\ll$ order; initially $(1 \ldots n)$.

$pc_i$: The non-input action currently enabled; initially `NIL`.

$op_i$: The current operation; initially `NIL`.

**SCAN_i:**

BEGIN SCAN_i

**Eff:**

$op_i \leftarrow \text{SCAN}_i$

$pc_i \leftarrow \text{SNAP}_i(\tilde{\ell}_i, \tilde{\sigma}_i)$

SNAP_i($\tilde{\ell}_i, \tilde{\sigma}_i$)

**Pre:**

$pc_i = \text{SNAP}_i(\tilde{\ell}_i, \tilde{\sigma}_i)$

**Eff:**

**If** $op_i = \text{SCAN}_i$ **then**

$\tilde{\sigma}_i \leftarrow$ the sequence of indexes where $j$ appears before $k$ in $\sigma_i$ iff $(t_j, j) \ll (t_k, k)$

$pc_i \leftarrow \text{ENDSCAN}_i(\tilde{\sigma}_i, \tilde{\sigma}_i)$

**If** $op_i = \text{LABEL}_i$ **then**

$nt_i \leftarrow \text{NEWLABEL}_i(\tilde{\ell}_i)$

$pc_i \leftarrow \text{UPDATE}_i((t_i, v_i), (nt_i, val_i))$

ENDSCAN_i($\tilde{\sigma}_i, \tilde{\sigma}_i$)

**Pre:**

$pc_i = \text{ENDSCAN}_i(\tilde{\sigma}_i, \tilde{\sigma}_i)$

**Eff:**

$pc_i \leftarrow \text{NIL}$

**LABEL_i:**

BEGIN LABEL_i

**Eff:**

$op_i \leftarrow \text{LABEL}_i$

$pc_i \leftarrow \text{SNAP}_i(\tilde{\ell}_i, \tilde{\sigma}_i)$

UPDATE_i((t_i, v_i), (nt_i, val_i))

**Pre:**

$pc_i = \text{UPDATE}_i((t_i, v_i), (nt_i, val_i))$

**Eff:**

$pc_i \leftarrow \text{ENDLABEL}_i$

ENDLABEL_i

**Pre:**

$pc_i = \text{ENDLABEL}_i$

**Eff:**

$pc_i \leftarrow \text{NIL}$

Figure 2: Precondition-Effect code for \textsc{ucTSS} and \textsc{bcTSS}
Figure 3: Code for $\text{newlabel}_i$ of $\text{uctss}$

$\text{uctss}$ uses labels that are non-negative real numbers. The ordering between labels is the usual $<$ order of $\mathbb{R}^+$. The ordering $\ll$ used in the $\text{order}_i$ action is a lexicographical order between label and process index pairs.

**Definition 3.1 (\ll order)** $(\ell_i, i) \ll (\ell_j, j)$ iff $\ell_i < \ell_j$ or $\ell_i = \ell_j$ and $i < j$.

4 A Bounded Concurrent Timestamp System

In this section we present our bounded implementation of a concurrent timestamp system, $\text{bctss}$. $\text{bctss}$ differs from $\text{uctss}$ in three ways: the structure of the labels, the order between labels, and the manner in which $\text{newlabel}_i$ determines new labels. In all other aspects $\text{bctss}$ and $\text{uctss}$ are identical. Recall that a label in $\text{uctss}$ is an element of $\mathbb{R}^+$. In $\text{bctss}$, labels are taken from a different domain. In order to construct the new domain we introduce the set $\mathcal{A} = \{1 \ldots 5\}$. We define the order $\prec_{\mathcal{A}}$ and the function $\text{next}$ on the elements of $\mathcal{A}$.

$$1 \prec_{\mathcal{A}} 2, 3, 4, 5; \quad 2 \prec_{\mathcal{A}} 3, 4, 5; \quad 3 \prec_{\mathcal{A}} 4; \quad 4 \prec_{\mathcal{A}} 5; \quad 5 \prec_{\mathcal{A}} 3.$$

The graph in Figure 4 represents $\prec_{\mathcal{A}}$, where $a \prec_{\mathcal{A}} b$ iff there is a directed edge from $b$ to $a$.

$$\text{next}(k) = \begin{cases} k + 1 & \text{if } k \in \{1, 2, 3, 4\} \\ 3 & \text{if } k = 5 \end{cases}$$

A $\text{bctss}$ label is an element of $\mathcal{A}^{n-1}$, where $n$ is the number of processes in the system. We refer to elements of $\mathcal{A}^{n-1}$ using array notation. Specifically, the $h^{th}$ digit of label $\ell$ will be denoted by $\ell[h]$. Since we have redefined the label type, we must specify the order that is to be used between elements of $\mathcal{A}^{n-1}$ for the $\ll$ order in the $\text{snap}_i$ action. The order between elements of $\mathcal{A}^{n-1}$ is represented by the symbol $\prec$ and will be a lexicographical order based on $\prec_{\mathcal{A}}$.

**Definition 4.1 (\prec order)** $\ell_i \prec \ell_j$ iff there exists $h \in \{1 \ldots n - 1\}$ such that $\ell_i[h'] = \ell_j[h']$ for all $h' < h$ and $\ell_i[h] \prec_{\mathcal{A}} \ell_j[h]$.

**Example 4.1** $4 \ldots 4.5.2 \prec 4 \ldots 4.3.1$

The intuition behind the label set $\mathcal{A}^{n-1}$ and the order $\prec$ can be best understood by examining the inadequacies of using a bounded set of natural numbers as the label set. Consider the natural numbers from 0 to $n$ with the usual $<$ ordering. As with $\text{uctss}$, ties between processes are broken based on process index. The obvious problem with this label set is deciding what happens when
some processes has the label \( n \) and another process needs a new, bigger label. The obvious solution is to wrap around and use the number 0. Then the ordering among labels would be the usual \(<\) ordering with the additional feature that \( n < 0 \).

Using a wrap around strategy provides a good solution for two processes. In particular consider the processes \( p_1 \) and \( p_2 \) with the label set \( \{0, 1, 2\} \). It is easy to see that this works since there will always be an extra number between the labels of \( p_1 \) and \( p_2 \) to make sure that they are totally ordered. However, the wrap around strategy does not work for three processes. Consider the following situation for three processes. Let each process \( p_i \) have label \( i \). Also assume that process \( p_2 \) with label 2 wants a new label that is bigger than the label 3 of \( p_3 \). Using our wrap around strategy, that label would be 0. However, now process \( p_2 \)'s label is ordered below that of \( p_1 \) which has a label 1, which violates the ordering properties of a timestamp system since \( p_1 \)'s current label was acquired before \( p_2 \) acquired the label 0. One solution might be to extend the set of numbers from which the labels are chose so that the wrap around happens later. However, it is easy to see that this will not help. In particular, processes \( p_2 \) and \( p_3 \) can ask for new labels alternately until one of them reaches the highest label. The first process to wrap around will encounter the same problem we just identified for \( n = 3 \). What is needed is the ability to create a cycle of numbers for processes \( p_2 \) and \( p_3 \) such that all numbers in that cycle are ordered above zero.

Now consider how \( \mathcal{A}^2 \) would create such a cycle. First note that the label set \( \mathcal{A} \) contains a size three cycle, (using the numbers 3, 4, 5). We saw above from the two processor example that a size three cycles can accommodate two processes. The labels in \( \mathcal{A}^2 \) that share the same first digit each form a size three cycle, that can accommodate two processes. Now consider the situation where \( p_1 \) starts out with label 3, \( p_2 \) starts out with label 3.4 and \( p_3 \) starts out with 4.3. Using \(<\) these labels are ordered in order of the process indexes. Now let \( p_2 \) and \( p_3 \) alternate picking new labels. They can use the size three cycles defined by the labels in \( \mathcal{A}^2 \) that start with the prefix 4. All labels in that cycles are ordered above \( p_1 \)'s label of 3.3. If \( p_0 \) now becomes active, it can jump to 5.3 which will be higher that the two labels used by \( p_2 \) and \( p_3 \).

The label set \( \mathcal{A}^{n-1} \) with the order \(<\) is the generalization of these ideas to \( n \) processes. In particular, any set of label from \( \mathcal{A}^{n-1} \) which agree on the first \( h \) digits form a size \( 3^{n-1-h} \) cycle, which consists of three ordered size \( 3^{n-1-h-1} \) cycles, each of which in turn consist of three ordered \( 3^{n-1-h-2} \) cycles, etc. Such a set of labels can accommodate \( n-h \) processes in any sequential execution. The cycle system construction is due to Israeli and Li [15].
This discussion has not identified a need for the numbers 1 and 2 in the set $\mathcal{A}$. These numbers are the key to making $\mathcal{A}$ work in concurrent executions. They are needed to deal with the fact that we use atomic snapshot objects as the underlying communications model. More powerful snapshot primitives that can atomically (i.e. in a single atomic step) read all of the labels and write one label will eliminate the need for these numbers (note that constructing an efficient implementation of such an object, even if one is given a compare-and-swap or other powerful operation, is not an obvious task). We will make our intuitive justification of the $\mathcal{A}^{n-1}$ label set with the order $< \!$ more concrete with some specific examples once we have introduced some additional notation.

The following lemma shows that any two labels in $\mathcal{A}$ are always totally ordered by the $< \!$ relation.

**Lemma 4.1** If $\ell_1$ and $\ell_2$ are elements of $\mathcal{A}^{n-1}$ then exactly one of the following is true: $\ell_1 < \! \ell_2$, $\ell_2 < \! \ell_1$, or $\ell_1 = \ell_2$.

**Proof:** If $a, b \in \mathcal{A}$, then by definition of $< \!$ exactly one of the following is true: $a < \! b$, $b < \! a$ or $a = b$. The lemma now follows since $< \!$ is a lexicographical order defined by $< \!$. ■

Next we define some notation and functions for bctss labels. Before giving the formal definitions we give intuitive definitions. Two labels are $h$-equivalent, $\equiv$, if their first $h$ digits are the same. The function $\text{nextlabel}(\ell, h)$ picks the label that is the same as $\ell$ for the first $h-1$ digits, changes the $h^{th}$ digit based on the next function, and sets the remaining digits to 1. The $\text{nextlabel}$ function is used to pick new labels during the label operation. Finally, the set $\text{cycle}(\ell, h)$ consists of the labels that are $h-1$-equivalent to $\ell$ and have the $h^{th}$ digit equal to 3,4,5.

**Definition 4.2** ($\equiv$ equivalence relation) For any $h \in \{0 \ldots n - 1\}$, $\ell_1 \equiv h \ell_2$ iff $\ell_1[h'] = \ell_2[h']$ for all $h' \leq h$. Note that $\ell_1 \equiv h - 1 \ell_2$ implies that $\ell_1 = \ell_2$.

**Definition 4.3** (nextlabel) For any $h \in \{1 \ldots n - 1\}$, $\ell' = \text{nextlabel}(\ell, h)$ iff $\ell' \equiv h - 1 \ell$, $\ell'[h] = \text{next}(\ell[h])$ and $\ell'[h'] = 1$ for all $h' \in \{h + 1 \ldots n - 1\}$.

**Definition 4.4** (cycle) For any $h \in \{1 \ldots n - 1\}$, $\ell' \in \text{cycle}(\ell, h)$ iff $\ell' \equiv h - 1 \ell$ and $\ell'[h] \in \{3, 4, 5\}$.

The following Lemma gives a neccessary and sufficient condition for three label in $\mathcal{A}^{n-1}$ to form a cycle under the $< \!$ ordering.

**Lemma 4.2** A set $\mathcal{L}$ of labels is not totally ordered by $< \!$ iff there exist $\ell_1, \ell_2, \ell_3 \in \mathcal{L}$ and $h \in \{1 \ldots n - 1\}$ such that $\ell_1 \equiv h - 1 \ell_2 \equiv h - 1 \ell_3$ and $\{\ell_1[h], \ell_2[h], \ell_3[h]\} = \{3, 4, 5\}$.

**Proof:** $\Rightarrow$ The $< \!$ ordering on $\mathcal{L}$ is irreflexive by definition and antisymmetric by Lemma 4.1. Therefore, it must be that transitivity does not hold. Specifically there exist $\ell_1, \ell_2, \ell_3 \in \mathcal{L}$ such that $\ell_1 < \! \ell_2 < \! \ell_3$, and $\ell_1 \not< \! \ell_3$. By Lemma 4.1 it cannot be that $\ell_1 = \ell_3$, therefore $\ell_3 < \! \ell_1$. Since $< \!$ is a lexicographical order, there must exist $h \in \{1 \ldots n - 1\}$ such that $\ell_1 \equiv h - 1 \ell_2 \equiv h - 1 \ell_3$ and $\ell_1[h] < \! \ell_2[h] < \! \ell_3[h]$ and $\ell_1[h] < \! \ell_3[h]$. Now by definition of $\mathcal{A}$, $\{\ell_1[h], \ell_2[h], \ell_3[h]\} = \{3, 4, 5\}$.

$\Leftarrow$ By definition of $\mathcal{A}$ we can conclude without loss of generality that $\ell_1[h] < \! \ell_2[h] < \! \ell_3[h]$ and $\ell_1[h] < \! \ell_3[h]$. Since $\ell_1 \equiv h - 1 \ell_2 \equiv h - 1 \ell_3$ and $< \!$ is a lexicographical order, $\ell_1 < \! \ell_2 < \! \ell_3$, and $\ell_1 \not< \! \ell_3$. Hence, $\ell_1, \ell_2$, and $\ell_3$ are not totally ordered. ■
We now define some functions on the states of BCTSS. In order to reason about the states of the system we introduce the notation $b.x$ to refer to the variable $x$ in state $b$. For a state $b$ and any label $\ell$ in state $b$. The set $\text{AGREE}(b, \ell, h)$ is the set of process indexes $j$ such that the $t_j$ label in state $b$ that is $h$-equivalent to the label $b.\ell$. $\text{NUM}(b, \ell, h)$ is the cardinality of $\text{AGREE}(b, \ell, h)$. Finally, $\text{NUM}_i(b, \ell, h)$ is the cardinality of $\text{AGREE}(b, \ell, h)$ once process $i$ is removed from $\text{AGREE}(b, \ell, h)$. We remind the reader that the following definitions are based on those in Figure 2.

**Definition 4.5** (AGREE) For any $h \in \{0 \ldots n - 1\}$, $\text{AGREE}(b, \ell, h) = \{j | b.t_j \equiv_b^h \ell\}$. ■

**Definition 4.6** (NUM) For any $h \in \{0 \ldots n - 1\}$, $\text{NUM}(b, \ell, h) = |\text{AGREE}(b, \ell, h)|$. ■

**Definition 4.7** (NUM) For any $h \in \{0 \ldots n - 1\}$, $\text{NUM}_i(b, \ell, h) = |\text{AGREE}(b, \ell, h) - \{i\}|$. ■

**Definition 4.8** (choice vector) A choice vector for state $b$ is any vector $(b.\ell_1 \ldots b.\ell_n)$ such that $b.\ell_i \in \{b.t_i, b.nt_i\}$ for each $i$. ■

**Definition 4.9** (TOT) $\text{TOT}(b) = true$ iff the set of values in every choice vector is totally ordered by $\prec$; otherwise $\text{TOT}(b) = false$. ■

Recall that the second difference betweenUCTSS and BCTSS is the $\ll$ order that is used in $\text{SNAP}_i$. We define $\ll$ for BCTSS lexicographically.

**Definition 4.10** ($\ll$ order) $(\ell_i, i) \ll (\ell_j, j)$ iff either $\ell_i \prec \ell_j$ or $\ell_i = \ell_j$ and $i < j$. ■

In any state $b$ in which $\text{TOT}(b) = true$, $\ll$ defines a total order.

We now define $b.t_{\text{max}}$ and $b.i_{\text{max}}$ for a state, $b$, in which $\text{TOT}(b) = true$. Consider the choice vector $(b.\ell_1 \ldots b.\ell_n)$. Since $\text{TOT}(b) = true$, there must exist $i \in \{1 \ldots n\}$ such that, for all $j \neq i$ and $j \in \{1 \ldots n\}$, $b.t_j \leq b.\ell_i$. Let $b.t_{\text{max}} = b.\ell_i$. Let $b.i_{\text{max}}$ be the largest index $j$ such that $b.t_j = b.t_{\text{max}}$.

The final difference between BCTSS and UCTSS is in the code for NEWLABEL. Recall that in UCTSS, NEWLABEL nondeterministically picks a real number that is larger than $t_{\text{max}}$. In BCTSS, NEWLABEL also picks the new label based on $t_{\text{max}}$. In states in which $\text{TOT}(b) = true$, $b.t_{\text{max}}$ and $b.i_{\text{max}}$ are defined. We let NEWLABEL be a no-op for states in which $\text{TOT}(b) = false$. In Section 5 we will show that $\text{TOT}(b) = true$ for all reachable states. When $i_{\text{max}}$ is defined and $i \neq i_{\text{max}}$, NEWLABEL finds the minimum $h$ such that at least $n - h$ $t$-labels, excluding $t_i$, agree with the prefix of $t_{\text{max}}$ up to and including the $h^{th}$ digit. Then the new label is the same as $t_{\text{max}}$ for the first $h - 1$ digits, it differs from $t_{\text{max}}$ at the $h^{th}$ digit based on the function NEXT, and its remaining digits are equal to 1. The code for NEWLABEL of BCTSS is given in Figure 5.

NEWLABEL finds the minimum integer $h$ such that FULL(h) returns true. We now show that such an $h$ exists in $\{1 \ldots n - 1\}$. The code that finds $h$ is executed only when $i \neq i_{\text{max}}$. Notice that $\text{NUM}_i(t_{\text{max}}, n - 1) \geq 1$ when $i \neq i_{\text{max}}$, hence FULL(h) returns true.

The initial values for the labels in BCTSS are: $t_i = nt_i = 1^{n-1}$, $v_i = (1 \ldots n)$, $v_i = (v_0 \ldots v_o)$, $\bar{i} = (1^{n-1} \ldots 1^{n-1})$, $v_i = val_i = v_o$, $op_i = \text{NIL}$ and $pc_i = \text{NIL}$.

We now return to our intuitive justification of the $A^{n-1}$ label set with the order $\prec$. Specifically we will strengthen our intuition using some examples. The examples will show how the following “invariants” are maintained:

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\[ \text{FULL}_i(h), \ h \in \{1 \ldots n - 1\} \]
\[ \text{if \ \text{NUM}_i(t_{max}, h) \geq n - h} \]
\[ \text{then return (true)} \]
\[ \text{else return (false)} \]

\[ \text{NEWLABEL}_i(t_i) \]
\[ \text{if } i \neq i_{\text{max}} \]
\[ \text{then } h' \leftarrow \text{minimum } h \in \{1 \ldots n - 1\} \text{ such that } \text{FULL}_i(h) = \text{true} \]
\[ \text{return (NEWLABEL}(t_{max}, h')) \]

Figure 5: Code for \text{NEWLABEL}_i of bctss

(1) For any reachable state \( b \) and any \( h \in \{1 \ldots n - 1\} \) consider any set of labels that agree on the first \( h - 1 \) digits. Then it will not be the case that \( h^{th} \) digits of that set of labels includes the numbers 3, 4 and 5. More formally, for any \( i \in \{1 \ldots n\} \): \( \{b.t_j[h] | b.t_j \in \text{CYCLE}(b.t_i, h)\} \neq \{3, 4, 5\} \).

(2) For any reachable state \( b \) and any \( h \in \{1 \ldots n - 1\} \) consider any set of labels that agree on the first \( h - 1 \) digits and have the \( h^{th} \) digit in the set \( \{3, 4, 5\} \). The cardinality of that set of labels is at most \( n - h + 1 \). More formally, for any \( i \in \{1 \ldots n\} \): \( |\text{CYCLE}(b.t_i, h)| \leq n - h + 1 \).

Maintaining the second invariant is the key to maintaining the first, and the first implies that \( \text{TOT}(b) = \text{true} \) when the choice vector is restricted to the \( t \)-labels. While the invariants provide good intuition about the correctness of the algorithm, it turns out that they are too general to be used the induction proof, and Theorem 5.1 must use a set of more refined inductive statements to capture the details of the possible concurrent behaviors.

The manner in which the invariants (1) and (2) are preserved, is explained via several examples. The first example considers a serial setting, while the second example considers a concurrent setting. The concurrent setting will illustrate the need for the numbers 1 and 2 in the \( A \) set.

For simplicity, the examples consider the case where \( n = 3 \). Thus the labels will be taken from \( A^2 \). For both examples, processes \( p_1, p_2, p_3 \) start out with labels \( t_1 = 3.4, t_2 = 3.5, \) and \( t_3 = 4.1 \). It is easy to see that the labels are totally ordered by the \( \prec \) ordering.

**Example 4.2** Assume that the following sequence of labeling operation are executed sequentially. Process \( p_1 \) performs a label operations that reads \( t_1, t_2 \) and \( t_3 \), and picks the new label \( t'_1 = 4.2 \) based on \text{NEWLABEL}(t_1) \). Process \( p_3 \) performs a label operations that reads the new label \( t'_1 \). It thus picks a label \( t'_3 \) with first digit 4, following the rule that the node chosen should be the “lowest node dominating all other nodes with labels.” This is actually the most basic rule implied by the definition of \text{NEWLABEL}.

Processes \( p_1 \) and \( p_2 \) can continue forever to choose \( t''_1 = 4.4, t''_3 = 4.5, t''_1 = 4.3... \) (that is, pick labels first digit 4 and second digit taken from \{3, 4, 5\}), maintaining the above invariants, because the set of labels with first digit 4 and second digit taken from \{3, 4, 5\} represent a size three cycle that can accomodate two processes.
If at some point \( p_2 \) picks a new label, \( L'_2 \), it will read the labels of both \( p_1 \) and \( p_3 \) has having the first digit 4. Any set of labels in \( A^2 \) that have the first digit in common can accomodate at most two processes. In particular, \( p_3 \)'s label operation will find that \( \text{num}_2(t_{max}) = 2 \). Since \( \text{full}_2(2) = \text{true} \), \( p_2 \) will pick \( t'_2 = 5.1 \), and so on...

From Example 4.2 the reader can see that given that the second “invariant” is maintained, only two processes have labels from the cycle \{4,3,4,4,4,5\}, the first “invariant” is readily maintained. The basis for guaranteeing the second “invariant,” is that the algorithm is structured so that node 1 on an arbitrary level \( h \) has at least \( n - h + 1 \) processes agree on it before any process can choose node 2. This implies that processes move to cycle nodes only from nodes 1 and 2. If there were by way of contradiction an \( (n - h + 2) \)-nd process \( p_i \) moving to a cycle node, say to a node 3, it would have had to see a maximal label on node 2, which in turn means node 1 would have had at least \( n - h + 1 \) labels when \( p_i \) performed its \text{SNAP}, so \( p_i \) would have detected \( n - h + 2 \) labels that agree with it on level \( h - 1 \) and would change its \( h - 1 \) level label and not choose a cycle label on level \( h \). The following is an example of the role of nodes 1 and 2.

**Example 4.3** Let processes \( p_2 \) and \( p_1 \) begin performing labeling operations concurrently, reading \( t_2 \), \( t_1 \) and \( t_3 \) and computing \text{newlabel}, such that \( nt_2 = nt_1 = 4.2 \). If they then complete their operations by writing their labels (i.e., do the \text{UPDATE} operation), they will choose the same label. Their labels can be ordered using their processor \text{ids}. If either process performed a subsequent label operation it would choose the label 5.1 since \( \text{num}_2(t_{max}, 1) = \text{num}_1(t_{max}, 1) = 2 \). Thus, neither process would choose a label in the cycle defined by the labels 4,3,4,4,4,5.

Now change the above scenario so that both processes do not complete their label operations. In particular, suppose that \( p_2 \) is stalled just before writing \( nt_2 = 4.2 \) to the shared variable \( t_2 \) (using the \text{UPDATE} operation), while \( p_1 \) writes \( t'_1 = nt_1 = 4.2 \). Now let process \( p_3 \) perform a label operation that reads the new label \( t'_1 = 4.2 \) and the old label \( t_2 = 3.5 \), thus picking \( t'_3 = 4.3 \). If processes \( p_1 \) and \( p_2 \) continue to pick new labels, they will pick them from the cycle defined by the labels 4,3,4,4,4,5, since they continue to read \( p_3 \)'s old label. At some point let \( p_2 \) complete its label operation writing \( t'_2 = nt_2 = 4.2 \). Now there are three labels \( 4, 3, 2 \) to 4 (two of them in \text{cycle}(t'_2, 2)). However, if \( p_2 \) now performs a subsequent labeling operation, it will read the labels of both \( p_1 \) and \( p_3 \) as being \( 3, 4 \) to 4. Since \( \text{num}_2(t_{max}, 1) = 2 \), \( \text{full}_2(1) = \text{true} \), so \( p_2 \) will pick the new label \( t''_2 = 5.1 \), not picking a label represented by a node in the cycle defined by the labels 4,3,4,4,4,5.

If the labels were not structured to include the values 1 and 2, which are not part of the cycle generated by the values 3, 4, and 5, then a process would always have a label that is represented by a node in a cycle. The reader can verify that the sequence of operations in this example, would cause the labels of \( p_1, p_2 \) and \( p_3 \) to end up each on a different nodes of the cycle defined by the labels 4,3,4,4,4,5, contradicting invariant (1).

5 Invariants

For use in the simulation proof we define the following invariants:

**Theorem 5.1** If \( b \) is a reachable state of \text{bctss} then, for all \( i \in \{1 \ldots n\} \):
I: $\text{tot}(b) = \text{true}$.

II: If $i = b.i_{\text{max}}$ then $b.t_i = b.nt_i$.

III: If $b.t_{\text{max}} < b.nt_i$ then there exists $h \in \{1 \ldots n - 1\}$ such that $b.nt_i = \text{nextlabel}(b.t_{\text{max}}, h)$.

IV: If $b.nt_i \leq b.t_{\text{max}}$ then for any $h \in \{1 \ldots n - 1\}$, if $b.t_i \overset{h}{=} b.t_{\text{max}}$ then $b.nt_i \overset{h}{=} b.t_{\text{max}}$.

V: For any $h \in \{1 \ldots n - 1\}$, if $b.nt_i \in \text{cycle}(b.t_{\text{max}}, h)$ then $b.t_i \overset{h}{=} b.t_{\text{max}}$.

VI: For any $h \in \{1 \ldots n - 1\}$,

- a: if $b.nt_i = \text{nextlabel}(b.t_{\text{max}}, h)$ then $\text{num}_i(b.t_{\text{max}}, h - 1) \geq n - h$.
- b: if $b.t_{\text{max}}[h] \neq 1$ then $\text{num}(b.t_{\text{max}}, h - 1) \geq n - h + 1$.

\[\Box\]

The following is an intuitive explanation of the inductive claims of Theorem 5.1 that hold for any system state.

I: The set of labels that for each process includes either its current label or its newly chosen label (yet unwritten) is totally ordered. This implies the first “invariant,” but is slightly stronger in the sense that it tracks the behavior of yet unwritten labels.

II: The process with the maximal label does not choose a new one.

III: The $\text{nextlabel}$ function defines a label’s successor for a given digit. This invariant states that a process’ new (not yet updated) label must be a successor of the maximum at some digit level $h$, even though the state (including the maximum) may have changed since the new label was picked.

IV: Consider a process $p_i$ that has chosen a new label which is no longer the maximum (due to subsequent new labels chosen by others) but has not yet updated the shared state. Then, for all processes whose new labels were chosen based on $p_i$’s current label, their choice would have been the same had they used $p_i$’s new label. This is due to the fact that updating the shared state does not affect the $\text{num}$ count.

V: If a process chooses a new label in the cycle at digit $h$, (i.e., $h \in \{3, 4, 5\}$), then it must be that its new label is the same as its current label for the first $h - 1$ digits. This implies that a process must choose either 1 or 2 before it can choose digits $\{3, 4, 5\}$.

VI: For any new label,

- a: if it is a successor of the max at digit $h$, then the number of labels that agree with max on the first $h$ digits is the maximum that can be accommodated by that $h$-digit prefix. In terms of the algorithm this means that a process uses a new prefix only when necessary.

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b: Once some process has chosen an $h$-th level digit that is greater than 1 (i.e., $\{2, 3, 4, 5\}$), then the number of labels that agree with it on the $h-1$ prefix must be at least the maximum that can be accommodated by that prefix.

Invariants I, II, and III are used in the simulation proof. We use an induction argument to show that all reachable states of bctss satisfy these invariants. The purpose of invariants IV - VI is to strengthen the induction hypothesis enough so that I can be proven. The only action that can cause invariant I to be violated is \text{SNAP}_i$ when $\text{op}_i = \text{LABEL}_i$. Specifically, we must show that the new $nt_i$ picked by \text{NEWLABEL}_i does not introduce any cycles in the $<$ order of the $t$-labels and $nt$-labels. Since the \text{NEWLABEL}_i code can examine the all of the $t$-labels, the code can be written to avoid any cycles involving $nt_i$ and the $t$-labels. However, the \text{NEWLABEL}_i code cannot examine the local $nt$-labels of the other processes. In order to show that cycles that include $nt_i$ and $nt$-labels are avoided, invariants IV and V are used to limit the possible values of the $nt$-labels based on the corresponding $t$-labels.

For example invariant IV implies that $nt_i \xrightarrow{h} t_i$ when $t_i \xrightarrow{h} t_{\max}$ for all $nt_i \preceq t_{\max}$. If $nt_i$ is in the cycle at level $h$, in other words $nt_i[h] \in \{3, 4, 5\}$, then invariant V states that $nt_i \xrightarrow{h-1} t_i$. Now assume that \text{NEWLABEL}_i picks $nt_i = \text{NEXTLABEL}(t_{\max}, h)$. Then the code for \text{NEWLABEL}_i, using the function $\text{FULL}_i$, limits the number number of $t$-labels that are $\xrightarrow{h-1} t_{\max}$ and consequently the number of $t$-labels that are $\xrightarrow{h-1} nt_i$. Now invariant V can be used to limit the number of $nt$-labels that could, by being in the cycle at level $h$, cause a cycle to occur with the new $nt_i$.

Invariant III gives information about the structure of $nt$-labels that are $\succ t_{\max}$. This information is used to determine how the new $nt_i$ is ordered with respect to any $nt$-labels that are $\succ t_{\max}$. Finally invariant VIb is used to prove invariant V, and invariant VIa is used to prove VIb. If a new label $nt_i$ is picked in the cycle at level $h$ then it must be that $t_{\max}[h] \neq 1$; hence VIb applies. VIb says that $\text{NUM}(t_{\max}, h-1) \geq n - h + 1$. The code for \text{NEWLABEL}_i insures that $\text{NUM}(t_{\max}, h-1) < n - h + 1$. Thus it must be the case that $t_i \xrightarrow{h-1} t_{\max}$. This is precisely what is required to prove invariant V.

The proof of Theorem 5.1 uses induction. It depends on a series of claims, one for the initial state and one for each action in the inductive step. Most of these claims appear in the final section since they use a fairly straightforward, if tedious, case analysis. This section presents the key claims associated with invariant I. In the following claims assume that state $b$ transitions to $b'$ using the \text{SNAP}_k(f_k, \vec{c}_k)$ action.

Claim 5.1.1 If $k = b, i_{\max}$ then $b'$ satisfies I - VI.

Proof: The definition of \text{SNAP}_k(f_k, \vec{c}_k) for bctss shows that no labels change. This suffices to show that $b'$ satisfies I - VI. 

So assume that $k \neq b, i_{\max}$ for the remainder of the proof of the lemma. By definition of \text{NEWLABEL}_k, $b'.nt_k = \text{NEXTLABEL}(b,t_{\max}, h')$ for some $h' \in \{1 \ldots n-1\}$. Fix $h'$. Note, by definition of \text{NEXTLABEL}, $b.t_{\max} \prec b'.nt_k$.

Claim 5.1.2 If $k \neq b, i_{\max}$ then $\text{NUM}(b,t_{\max}, h') = \text{NUM}(b,t_{\max}, h' - 1) = n - h'$.

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Proof: By definition of \( \text{newlabel}_k \), \( \text{full}_k(h') \) returns true in state \( b \), so \( \text{num}_k(b.t_{\text{max}}, h') \geq n - h' \).
Moreover, \( \text{full}_k(h' - 1) \) returns false in state \( b \), therefore \( \text{num}_k(b.t_{\text{max}}, h' - 1) < n - (h' - 1) \). But by definition, \( \text{num}_k(b.t_{\text{max}}, h' - 1) \geq \text{num}_k(b.t_{\text{max}}, h') \) so \( \text{num}_k(b.t_{\text{max}}, h' - 1) = \text{num}_k(b.t_{\text{max}}, h') = n - h' \).

Claim 5.1.3 If \( k \neq b.i_{\text{max}} \) then \( I \) is true in \( b' \).

Proof: For a contradiction assume that \( \text{tot}(b') = \text{false} \). Then there must exist a choice vector \( C \) whose values are not totally ordered. By Lemma 4.2, there exists \( b'.t_i, b'.t_j, t' \in C \) such that \( b'.t_i \stackrel{h-1}{=} b'.t_j \stackrel{h-1}{=} b'.t_z \) and \( \{ b'.t_i[h], t'.t_j[h], b'.t_z[h] \} = \{3, 4, 5\} \) for some \( h \in \{1 \ldots n - 1\} \). Since \( b'.t_i, b'.t_j \) and \( b'.t_z \) are elements of a choice vector, \( b'.t_i \in \{ b'.t_i, b'.t_z \}, b'.t_j \in \{ b'.t_j, b'.t_z \}, b'.t_z \in \{ b'.t_z, b'.t_z \} \) and \( i \neq z, j \neq z, j \neq i \). By I for state \( b \), \( \text{tot}(b) = \text{true} \). Therefore the values of \( C \) for state \( b \) must be totally ordered. The only label that changes as a result of the action is \( nt_k \).
Consequently, we can assume without loss of generality that \( b'.t_z = b'.t_z \) and \( z = k \). Furthermore, since \( i \neq k \) and \( j \neq k \), \( t_i \) and \( t_j \) do not change as a result of the action. Thus, \( b'.t_i = b'.t_i \) and \( b'.t_j = b'.t_j \).
Now we can conclude that:

\[
b'.t_i \stackrel{h-1}{=} b'.t_j \stackrel{h-1}{=} b'.t_k \quad \text{and} \quad \{ b'.t_i[h], b'.t_j[h], b'.t_k[h] \} = \{3, 4, 5\}. \tag{1}
\]

Recall that \( b'.t_k = \text{nextlabel}(b.t_{\text{max}}, h') \). We will now show that \( h = h' \). Let \( z = b.i_{\text{max}} \), then \( b.t_z = b.t_{\text{max}} \). Since \( k \neq b.i_{\text{max}} \), \( k \neq z \). The definition of \( \text{nextlabel} \) implies that \( b.t_z \stackrel{h'}{=} b'.t_k \).
For a contradiction assume that \( h < h' \). Now substitute \( b.t_z \) for \( b'.t_k \) in Equation 1 to conclude that \( b'.t_i \stackrel{h-1}{=} b'.t_j \stackrel{h-1}{=} b.t_z \) and \( \{ b'.t_i[h], b'.t_j[h], b.t_z[h] \} = \{3, 4, 5\} \). By Lemma 4.2 any set of labels containing \( b'.t_i, b'.t_j, \) and \( b.t_z \) is not totally ordered. We now show that \( i \neq z \) and \( j \neq z \) since this will allow us to conclude that there exists a choice vector that includes \( b'.t_i, b'.t_j, \) and \( b.t_z \). Since \( \{ b'.t_i[h], b'.t_j[h], b.t_z[h] \} = \{3, 4, 5\} \), and \( b.t_i \in \{ b.t_i, b.t_z \} \) either \( b.t_i[h] \neq b.t_z[h] \) or \( b.t_i[h] \neq b.t_z[h] \).
If \( i = z \) the former is clearly impossible and the later is impossible since \( b.t_i = b.t_z \) by invariant \( \Pi \). Thus \( i \neq z \).
The same argument shows that \( j \neq z \).
Now we have a choice vector for state \( b \) whose values are not totally ordered. The existence of such a choice vector contradicts invariant \( I \) for state \( b \). Thus \( h \neq h' \). The definition of \( \text{nextlabel} \) implies that \( b'.t_k[h'] = 1 \) for all \( h' > h' \). Since \( b'.t_k[h] \in \{3, 4, 5\}, h \neq h' \).
Now \( h \neq h' \) and \( h \neq h' \) so \( h = h' \).

We now construct a set of labels which is not totally ordered and which includes \( b.t_{\text{max}} \) and \( b'.t_k \).
First show that \( b.t_{\text{max}}[h'] \in \{3, 4, 5\} \). Since \( b'.t_k[h'] \in \{3, 4, 5\} \), the definition of \( \text{nextlabel} \) implies that \( b.t_{\text{max}}[h'] \in \{2, 3, 4, 5\} \). We proceed by showing that \( b.t_{\text{max}}[h'] \neq 2 \). In order to reach a contradiction we assume that \( b.t_{\text{max}}[h'] = 2 \).
Since \( b.t_{\text{max}} \stackrel{h'}{=} b'.t_k \) and \( b'.t_k \stackrel{h'}{=} b'.t_i \), \( b.t_{\text{max}} \stackrel{h'}{=} b'.t_i \).
Furthermore, \( b.t_{\text{max}}[h'] = 2 \) and \( b.t_i[h'] \in \{3, 4, 5\} \) thus \( b.t_{\text{max}}[h'] \prec_A b.t_i[h'] \).
Consequently, \( b.t_{\text{max}} < b.t_i \).
We consider the cases \( b.t_i = b.t_i \) and \( b.t_i = b.t_i \).
When \( b.t_i = b.t_i \), \( b.t_{\text{max}} < b.t_i \), which contradicts the definition of \( b.t_{\text{max}} \). Thus, this case cannot arise.
When \( b.t_i = b.t_i \), \( b.t_{\text{max}} < b.t_i \). Now invariant \( \text{III} \) and the definition of \( \text{nextlabel} \) imply that \( b.t_i[h'] = b.t_{\text{max}}[h'] \) or \( b.t_i[h'] = \text{next}(b.t_{\text{max}}[h']) \).
Thus, when \( b.t_{\text{max}}[h'] = 2 \), \( b.t_i[h'] \in \{4, 5\} \).
Therefore we can conclude that \( b.t_i[h'] \in \{4, 5\} \) when \( b.t_{\text{max}}[h'] = 2 \). Using the same argument we can show that \( b.t_i[h'] \in \{4, 5\} \) when \( b.t_{\text{max}}[h'] = 2 \). This contradicts
Equation 1 according to which \( \{b_\ell_i[h'], b_\ell_j[h'], b'_\cdot n_{t_k}[h']\} = \{3, 4, 5\} \). Thus \( b_{\cdot t_{max}}[h'] \neq 2 \) and \( b_{\cdot t_{max}}[h'] \in \{3, 4, 5\} \).

Since \( \{b_\ell_i[h'], b_\ell_j[h'], b'_\cdot n_{t_k}[h']\} = \{3, 4, 5\} \), using the definition of \( \prec_A \), we can assume without loss of generality that:

\[ b_\ell_i[h'] \prec_A b_\ell_j[h'] \prec_A b'_\cdot n_{t_k}[h'] \quad \text{and} \quad b_\ell_i[h'] \not\prec_A b'_\cdot n_{t_k}[h']. \tag{2} \]

Recall that \( z = b_{\cdot t_{max}}, b_{\cdot t_z} = b_{\cdot t_{max}}, b_{\cdot t_z} \overset{h'-1}{=} b'_\cdot n_{t_k}, \) and \( b_{\ell_i}[h'] \prec_A b'_\cdot n_{t_k}[h'] \). Hence, we can replace \( b_\ell_j \) by \( b_{\cdot t_{max}} \) in Equation 1 and Equation 2 which yields the following:

\[ b_\ell_i \overset{h'}{=} b_{\cdot t_{max}} \overset{h'}{=} b'_\cdot n_{t_k} \quad \text{and} \quad \{b_\ell_i[h], b_{\cdot t_{max}}[h], b'_\cdot n_{t_k}[h]\} = \{3, 4, 5\}, \tag{3} \]

\[ b_\ell_i[h'] \prec_A b_{\cdot t_{max}[h']} \prec_A b'_\cdot n_{t_{k}}[h'] \quad \text{and} \quad b_\ell_i[h'] \not\prec_A b'_\cdot n_{t_{k}}[h']. \tag{4} \]

Consequently,

\[ b_\ell_i \prec b_{\cdot t_{max}} \prec b'_\cdot n_{t_k} \quad \text{and} \quad b_\ell_i \not\prec b'_\cdot n_{t_k}, \tag{5} \]

\[ \{b_\ell_i, b_{\cdot t_{max}}, b'_\cdot n_{t_k}\} \subseteq \text{CYCLE}(b_{\cdot t_{max}}, h'). \tag{6} \]

Consider the cases \( b_\ell_i = b_{\cdot n_{t_k}} \) and \( b_\ell_i = b_{\cdot t_i} \) separately:

\( b_{\cdot n_{t_k}} \): Since \( b_{\cdot n_{t_k}} \in \text{CYCLE}(b_{\cdot t_{max}}, h') \), \( V \) for state \( b \) shows that \( b_{\cdot t_i} \overset{h'-1}{=} b_{\cdot t_{max}} \). By Claim 5.1.2 \( \text{NUM}_k(b_{\cdot t_{max}}, h'-1) = \text{NUM}_k(b_{\cdot t_{max}}, h') \). Therefore, since \( i \neq k \), \( b_{\cdot t_i} \overset{h'-1}{=} b_{\cdot t_{max}} \) implies that \( b_{\cdot n_{t_k}} \overset{h'}{=} b_{\cdot t_{max}} \). Now, from IV for state \( b \) and the fact that \( b_{\cdot n_{t_k}} \prec b_{\cdot t_{max}} \), it follows that \( b_{\cdot n_{t_k}} \overset{h'}{=} b_{\cdot t_{max}} \), a contradiction to Equation 4 according to which \( b_{\cdot n_{t_k}}[h'] \prec_A b_{\cdot t_{max}}[h'] \).

\( b_{\cdot t_i} \): By Claim 5.1.2, \( \text{NUM}_k(b_{\cdot t_{max}}, h'-1) = \text{NUM}_k(b_{\cdot t_{max}}, h') \). Therefore, since \( i \neq k \), \( b_{\cdot t_i} \overset{h'-1}{=} b_{\cdot t_{max}} \) implies that \( b_{\cdot t_i} \overset{h'}{=} b_{\cdot t_{max}} \). Now, \( b_{\cdot t_i} \overset{h'}{=} b_{\cdot t_{max}} \) contradicts Equation 4 according to which \( b_{\cdot t_i}[h'] \prec_A b_{\cdot t_{max}}[h'] \).

We have reached a contradiction in each case. Consequently, there exists no choice vector such that its values are not totally ordered. Hence, \( \text{TOT}(b') = \text{true} \). \)

**Proof:** (For Theorem 5.1) We proceed by induction on the length of the execution ending in the reachable state \( b \). The base case is established by Lemma 7.1. The induction step is a case analysis based on the action \( \pi \), where \( (b', \pi, b') \) is a step in the execution. If \( \pi \in \{\text{BEGINSCAN}_k, \text{ENDSCAN}_k(\tilde{\sigma}_k, \tilde{\sigma}_k), \text{BEGINLABEL}_k(\text{val}_k), \text{ENDLABEL}_k\} \), the induction step follows from Lemma 7.2. If \( \pi = \text{UPDATE}_{k}((t_{k}, v_{k}), (n_{t_{k}}, \text{val}_k)) \), the induction step follows from Lemma 7.3. If \( \pi = \text{SNAP}_{k}(\tilde{t}_{k}, \tilde{\sigma}_k) \), the induction step follows from Lemma 7.4. \]
6 The Simulation Proof

In this section we use Theorem 2.1 to show that fairbehls(bctss) ⊆ fairbehls(uctss). This implies that bctss implements uctss. In order to use Theorem 2.1, we define the relation \( R \) between the states of bctss and the states of uctss as follows:

**Definition 6.1 (relation \( R \))** If \( b \) is a state of bctss and \( u \) is a state of uctss then \((b, u) \in R\) iff for all \( i, j \in \{1 \ldots n\}, i \neq j\):

1. \( b.t_i = u.t_i \).
2. \( b.t_j < b.t_i \) iff \( u.t_j < u.t_i \),
   \( b.nt_j < b.nt_i \) iff \( u.nt_j < u.nt_i \),
   \( b.t_j < b.nt_i \) iff \( u.t_j < u.nt_i \),
   \( b.nt_j < b.t_i \) iff \( u.nt_j < u.t_i \).
3. \( b.v_i = u.v_i \).
4. \( b.val_i = u.val_i \).
5. \( b.\bar{v}_i = u.\bar{v}_i \).
6. \( b.op_i = u.op_i \).
7. \( b.pc_i = u.pc_i \).

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Parts 1 and 5 ensure that a process \( p_i \) returns the same response to a scan request in bctss and in uctss. Recall that \( \bar{\sigma}_i \) contains the order of the labels that was last observed by \( p_i \). Part 2 states that the \(<\) ordering of any choice vector from bctss is the same as the \(<\) ordering of the corresponding labels from uctss. Notice that part 2 gives no information about the relation between \( t_i \) and \( nt_i \). Parts 3 and 5 ensure that bctss and uctss associate values with labels in the same manner. Part 6 ensures that uctss and bctss will execute the same part of the snap\(_i\) action code. Finally, part 7 ensures that uctss and bctss will be able to execute the corresponding action during each state transition.

The following lemma proves that the first of the three assumptions required by Theorem 2.1 is true.

**Lemma 6.1** For the initial state \( b \) of bctss, there exists an initial state \( u \) of uctss such that \((b, u) \in R\).

**Proof:** In the initial states \( b \) of bctss and \( u \) of uctss, \( \bar{\sigma}_i = (1 \ldots n) \) for all \( i \in \{1 \ldots n\} \). Hence part 1 of \( R \) is satisfied. Part 2 is satisfied since \( t_i = nt_j \) for all \( i, j \in \{1 \ldots n\} \) in both bctss and uctss. Parts 3 - 5 are satisfied since \( \bar{v}_i = (0 \ldots 0) \) and \( v_i = val_i = 0 \) for all \( i \in \{1 \ldots n\} \) in both bctss and uctss. Parts 6 and 7 of \( R \) is satisfied for the initial states since \( op_i = pc_i = \text{NIL} \) in both systems.

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The following lemma shows that the mapping $\mathbb{r}$ is preserved by all of the actions of BCTSS. This lemma proves that the second of the three assumptions required by Theorem 2.1 is true.

**Lemma 6.2** Let $b$ be a reachable state of BCTSS and $u$ be a reachable state of UCTSS such that $(b, u) \in \mathbb{r}$. If $(b, \pi, b')$ is a step of BCTSS then, there exists $u'$ such that $(u, \pi, u')$ is a step of UCTSS and $(b', u') \in \mathbb{r}$.

**Proof:** We proceed by case analysis on $\pi$.

Case $\pi \in \{\text{BEGIN SCAN}_k, \text{END SCAN}_k(\tilde{\sigma}_k, \tilde{\nu}_k), \text{END LABEL}_k\}$:

Since $(b, u) \in \mathbb{r}$, we can conclude that $b, pc_k = u, pc_k$, $b, \tilde{\sigma}_k = u, \tilde{\sigma}_k$, and $b, \tilde{\nu}_k = u, \tilde{\nu}_k$. Hence, $\pi$ is enabled in $u$. Let $u'$ be the unique state of UCTSS such that $(u, \pi, u')$ is a step of UCTSS. In both BCTSS and UCTSS only $op_k$ and $pc_k$ change as a result of $\pi$. Inspection of the code in Figure 2 shows that $b', op_k = u', op_k$ and $b', pc_k = u', pc_k$. This suffices to show that $(b', u') \in \mathbb{r}$.

Case: $\pi = \text{BEGIN LABEL}_k(val_k)$:

Since $\text{BEGIN LABEL}_k(val_k)$ is an input action, it is clearly enabled in state $u$. Let $u'$ be the unique state of UCTSS such that $(u, \pi, u')$ is a step of UCTSS. Only $val_k$, $op_k$, and $pc_k$ change as a result of the action. By definition of the action $b'.val_k = u'.val_k$. Furthermore $b'.op_k = u'.op_k = \text{LABEL}_k$ and $b'.pc_k = u'.pc_k = \text{SCAN}_k$. This suffices to show that $(b', u') \in \mathbb{r}$.

Case $\pi = \text{SNAP}_k(\tilde{\nu}_k, \tilde{\nu}_k)$ when $b, op_k = \text{SCAN}_k$:

Since $(b, u) \in \mathbb{r}$, $b, pc_k = u, pc_k$. Hence, $\pi$ is enabled in $u$. Furthermore $u', op_k = b, op_k = \text{SCAN}_k$. Let $u'$ be the unique state such that $(u, \pi, u')$ is a step of UCTSS.

$\text{SNAP}_k(\tilde{\nu}_k, \tilde{\nu}_k)$, when $op_k = \text{SCAN}_k$, determines $\tilde{\sigma}_k$ based on the $\ll$ ordering. Recall that $\ll$ is a lexicographical order defined by the order between the $t$-labels, using $<$ for BCTSS and $\prec$ for UCTSS, and the order between the process indices. By assumption $(b, u) \in \mathbb{r}$. This implies that $b.t_i < b.t_j$ iff $u.t_i < u.t_j$ for all $i, j \in \{1 \ldots k\}$; thus $\text{SNAP}_k(\tilde{\nu}_k, \tilde{\nu}_k)$ will produce the same ordering for BCTSS and UCTSS. Hence $b', \tilde{\sigma}_k = u', \tilde{\sigma}_k$. Furthermore, part 3 of $\mathbb{r}$ implies that $b', \tilde{\nu}_k = u', \tilde{\nu}_k$. Figure 2 shows $b', pc_k = u', pc_k = \text{END SCAN}_k(\tilde{\sigma}_k, \tilde{\nu}_k)$. Only $\tilde{\sigma}_k$, $\tilde{\nu}_k$, and $pc_k$ change as a result of the action and thus we can conclude that $(b', u') \in \mathbb{r}$.

Case $\pi = \text{SNAP}_k(\tilde{\nu}_k, \tilde{\nu}_k)$ when $b, op_k = \text{LABEL}_k$:

Since $(b, u) \in \mathbb{r}$, $b, pc_k = u, pc_k$. Hence, $\pi$ is enabled in $u$. Furthermore $u, op_k = b, op_k = \text{LABEL}_k$. There are two cases: $k = b, i_{max}$ and $k \neq b, i_{max}$.

We first consider the case $k = b, i_{max}$. Since $(b, u) \in \mathbb{r}$, part 2 of $\mathbb{r}$ implies that $b, i_{max} = u, i_{max}$. Hence, $k = u, i_{max}$. Let $u'$ be the unique state such that $(u, \pi, u')$ is a step of UCTSS. Now the definition of $\text{NEW LABEL}_k$ for BCTSS and UCTSS shows that only $pc_k$ changes for both BCTSS and UCTSS. Figure 2 shows $b', pc_k = u', pc_k = \text{UPDATE}_k((t_k, v_k), (n_k, val_k))$. This suffices to show that $(b', u') \in \mathbb{r}$.

So assume that $k \neq b, i_{max}$ for the remainder of the proof of this case. Since $(b, u) \in \mathbb{r}$, part 2 of $\mathbb{r}$ implies that $b, i_{max} = u, i_{max}$. Hence, $k \neq u, i_{max}$. In this case there are many states $u'$ such that $(u, \pi, u')$ is a step of UCTSS; these states differ only by the value of $u', nt_k$. We now define a particular value $u', nt_k$ and hence a particular state $u'$. 

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Define $S = \{i \mid i \neq k \text{ and } b.t_{\text{max}} < b.nt_i\}$. Let $z = b.i_{\text{max}}$, then $b.t_z = b.t_{\text{max}}$. Invariant II shows that $b.nt_z = b.t_z$. Hence, $b.nt_z = b.t_{\text{max}}$. This implies that $z \not\in S$. Thus, $b.i_{\text{max}} \not\in S$. For all $i \in S$, III for state $b$ shows that $b.nt_i = \text{nextlabel}(b.t_{\text{max}}, h_i)$ for some $h_i \in \{1 \ldots n - 1\}$. Furthermore, the definition of \text{newlabel}$_k$ implies that $b'.nt_k = \text{nextlabel}(b.t_{\text{max}}, h_k)$ for some $h_k \in \{1 \ldots n - 1\}$. Define:

$$S_1 = \{i \mid i \in S, h_i > h_k\}, \quad S_2 = \{i \mid i \in S, h_i = h_k\} \quad \text{and} \quad S_3 = \{i \mid i \in S, h_i < h_k\}. \quad (7)$$

Note that:

$$S_1 \cap S_2 = S_2 \cap S_3 = S_1 \cap S_3 = \emptyset \quad \text{and} \quad S_1 \cup S_2 \cup S_3 = S. \quad (8)$$

Since $<$ is a lexicographical order, the order between any two labels in \text{BCTSS} is determined by the first digit at which they differ. Therefore, for any $i_1 \in S_1$, $i_2 \in S_2$, and $i_3 \in S_3$, it is the case that:

$$b.t_{\text{max}} < b.nt_{i_1} < b.nt_{i_2} = b'.nt_k < b.nt_{i_3}. \quad (9)$$

Recall $z = b.i_{\text{max}}$. Thus, $b.t_z < b.nt_{i_1} < b.nt_{i_2} = b'.nt_k < b.nt_{i_3}$. Since $z \not\in S$ and $(b, u) \in r$, part 2 of r now shows that $u.t_z < u.nt_{i_1} < u.nt_{i_2} < u.nt_{i_3}$. Since $b.i_{\text{max}} = u.i_{\text{max}}$, $z = u.i_{\text{max}}$ and $u.t_z = u.t_{\text{max}}$. This shows that:

$$u.t_{\text{max}} < u.nt_{i_1} < u.nt_{i_2} < u.nt_{i_3}. \quad (10)$$

We use the following rules for picking $u'.nt_k$. If $S_2 \neq \emptyset$, then $u'.nt_k = u.nt_i$ for any $i \in S_2$. If on the other hand $S_2 = \emptyset$, define $u.nt_{\text{max}}$ and $u.nt_{\text{min}}$ as follows: $u.nt_{\text{max}} = \max(u.nt_i \mid i \in S_1)$ if $S_1 \neq \emptyset$, otherwise $u.nt_{\text{max}} = u.t_{\text{max}}$. $u.nt_{\text{min}} = \min(u.nt_i \mid i \in S_3)$ if $S_3 \neq \emptyset$, otherwise $u.nt_{\text{min}} = \infty$. Choose any $u'.nt_k$ such that $u.nt_{\text{max}} < u'.nt_k < u.nt_{\text{min}}$. For any $i_1 \in S_1, i_2 \in S_2$, and $i_3 \in S_3$, the two rules and Equation 10 imply that:

$$u.t_{\text{max}} < u.nt_{i_1} < u.nt_{i_2} = u'.nt_k < u.nt_{i_3}. \quad (11)$$

With both rules for choosing $u'.nt_k$, $u.t_{\text{max}} < u'.nt_k$. Hence, there exists an $X \in \mathbb{R}^+ \cup \{\infty\}$ such that $u'.nt_k = u.t_{\text{max}} + X$.

We show that $(b', u') \in r$. Only $nt_k$ and $pc_k$ change as a result of the action. Figure 2 shows $b'.pc_k = u'.pc_k = \text{update}_b((t_k, v_k), (nt_k, va_k))$. Consequently, $(b', u') \in r$ if we can show that part 2 of r holds for states $b'$ and $u'$. For part 2 of the relation there are four cases to consider. All other cases do not involve $b'.nt_k$. Let $i \in \{1 \ldots n\}$ and $i \neq k$:

1. $b'.nt_k < b'.t_i$ if $u'.nt_k < u'.t_i$.
2. $b'.t_i < b'.nt_k$ if $u'.t_i < u'.nt_k$:

Since no $t$-labels change, $b'.t_{\text{max}} = b.t_{\text{max}}$ and $b'.i_{\text{max}} = b.i_{\text{max}}$. Recall that $k \neq b.i_{\text{max}}$, hence $b'.nt_k = \text{nextlabel}(b'.t_{\text{max}}, h_k)$ and $b'.t_{\text{max}} = b.t_{\text{max}} < b'.nt_k$ as a result of the action. Furthermore, $b'.t_i = b.t_i$. Therefore, $b'.t_i < b'.t_{\text{max}} < b'.nt_k$. Let $z = b'.i_{\text{max}}$. In this case $z \neq k$ and $b'.t_z = b.t_{\text{max}}$. Since $i \neq k$, $z \neq k$ and $b'.t_z = b'.t_{\text{max}}$, there exists a choice vector that includes $b'.t_i, b'.t_{\text{max}}$, and $b'.nt_k$. By invariant 1 the values of this choice vector are totally ordered by $\prec$. Therefore, $b'.t_i < b'.t_{\text{max}} < b'.nt_k$ implies that $b'.t_i < b'.nt_k$.

Similarly, since $k \neq u.i_{\text{max}}, u'.t_{\text{max}} = u.t_{\text{max}} < u'.nt_k$ as a result of the action. Furthermore, $u'.t_i = u.t_i$. Therefore $u'.t_i < u'.t_{\text{max}} < u'.nt_k$. This implies that $u'.t_i < u'.nt_k$.
2. $b'.nt_i < b'.nt_l$ iff $u'.nt_i < u'.nt_l$.
   $b'.nt_k < b'.nt_l$ iff $u'.nt_k < u'.nt_l$.

We can divide the $nt$-labels of $uctss$ into two disjoint sets: Recall that $S = \{j \mid j \neq k$ and $b.t_{max} < b.nt_j\}$. Define $T = \{j \mid j \neq k$ and $b.t_{max} \geq b.nt_j\}$. Similarly, define $S_u = \{j \mid j \neq k$ and $u.t_{max} < u.nt_j\}$. Define $T_u = \{j \mid j \neq k$ and $u.t_{max} \geq u.nt_j\}$. By part 2 of $r$ and the fact that $(b, u) \in r$, $S = S_u$ and $T = T_u$. Consider $i \in T$ and $i \in S$ separately.

Suppose $i \in T$. Since $i \neq k$, $b'.nt_i = b.nt_i$. Therefore $b'.nt_i < b'.t_{max} < b'.nt_k$. Let $z = b'.i_{max}$. In this case $z \neq k$ and $b'.t_z = b'.t_{max}$. Since $i \neq k$, $z \neq k$ and $b'.t_z = b'.t_{max}$, there exists a choice vector that includes $b'.nt_i, b'.t_{max}$, and $b'.nt_k$. By invariant 1 the values of this choice vector are totally ordered by $\prec$. Therefore, $b'.nt_i < b'.t_{max} < b'.nt_k$ implies that $b'.nt_i < b'.nt_k$. Similarly, $u'.nt_i = u.nt_i$, since $i \neq k$. Therefore, $u'.nt_i < u'.t_{max} < u'.nt_k$. This implies that $u'.nt_i < u'.nt_l$.

Now suppose $i \in S$. Consider any $i_1 \in S_1, i_2 \in S_2$, and $i_3 \in S_3$ where $S_1, S_2, S_3$ are defined by Equation 7. Since $k \notin S$, $b'.nt_j = b.nt_j$ and $u'.nt_j = u.nt_j$ for all $j \in S$. Consequently Equation 9 and Equation 11 show that $b.t_{max} < b'.nt_{i_1}, b'.nt_{i_2} = b'.nt_k < b'.nt_{i_3}$ and $u.t_{max} < u'.nt_{i_1}, u'.nt_{i_2} = u'.nt_k < u'.nt_{i_3}$. Using these facts we now consider the following cases: $i \in S_1$, $i \in S_2$, and $i \in S_3$. If $i \in S_1$, then $b'.nt_i < b'.nt_k$ and $u'.nt_i < u'.nt_k$. If $i \in S_2$, then $b'.nt_i = b'.nt_k$ and $u'.nt_i = u'.nt_k$. If $i \in S_3$, then $b'.nt_i < b'.nt_k$ and $u'.nt_k < u'.nt_i$.

Case $\pi = \text{UPDATE}_k((t_k, v_k), (nt_k, \text{val}_k))$:

Since $(b, u) \in r$, $b.pc_k = u.pc_k$. Hence, $\pi$ is enabled in $u$. Let $u'$ be the unique state such that $(u, \pi, u')$ is a step of $uctss$.

Only $v_k, t_k$ and $pc_k$ change as a result of the action. Since $(b, u) \in r$, part 4 of $r$ shows that $b.val_k = u.val_k$. Thus, $b'.vk = u'.vk$. Figure 2 shows $b'.pc_k = u'.pc_k = \text{ENDLABEL}_k$. Consequently, $(b', u') \in r$ if we can show that part 2 of $r$ holds for states $b'$ and $u'$. For part 2 of $r$ there are four cases to consider. All other cases are immediate since they do not involve $t_k$, and since $t_k$ is the only label that changes as a result of the action. Let $i \in \{1, \ldots, n\}$ and $i \neq k$:

1. $b'.t_k < b'.t_i$ iff $u'.t_k < u'.t_i$.

   Since $(b, u) \in r$ and $t_k$ is the only label that changes, $b.nt_k < b'.t_i$ iff $u.nt_k < u'.t_i$. As a result of the action, $b'.t_k = b.nt_k$ and $u'.t_k = u.nt_k$. Hence $b'.t_k < b'.t_i$ iff $u'.t_k < u'.t_i$.

2. $b'.t_i < b'.t_k$ iff $u'.t_i < u'.t_k$,
   $b'.nt_i < b'.nt_k$ iff $u'.nt_i < u'.nt_k$,
   $b'.t_k < b'.nt_i$ iff $u'.t_k < u'.nt_i$;

   For all three statements, the reasoning is similar to that of case 1.

We can now conclude that $bctss$ correctly implements $uctss$.

**Theorem 6.3** $bctss$ implements $uctss$. 

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Proof: By definition of BCTSS and UCTSS, \( \text{sig}(\text{BCTSS}) = \text{sig}(\text{UCTSS}) \) and \( \text{part}(\text{BCTSS}) = \text{part}(\text{UCTSS}) \). Lemma 6.1, and Lemma 6.2 show that BCTSS and UCTSS satisfy the first two conditions of Theorem 2.1. For the third condition note that action \( \pi \) is enabled in UCTSS if and only if \( \pi \) is enabled in BCTSS. Consequently, Theorem 2.1 shows that \( \text{fairbeh}(\text{BCTSS}) \subseteq \text{fairbeh}(\text{UCTSS}) \). Thus BCTSS implements UCTSS.

7 Claims for Proof of Theorem 5.1

This section contains the bulk of the claims needed for Theorem 5.1.

Lemma 7.1 The initial state \( b \) of BCTSS, satisfies invariants I - VI.

Proof: This follows from the fact that \( b.t_i = b.nt_j = 1^{n-1} \) for all \( i, j \in \{1 \ldots n\} \).

Lemma 7.2 Let \( b \) be a state of BCTSS that satisfies I - VI. If \( (b, \pi, b') \) is a step of BCTSS where \( \pi \in \{ \text{BEGINscan}_k, \text{ENDscan}_k(\sigma, \sigma_k), \text{BEGINlabel}_k(\text{val}_k), \text{ENdlabel}_k \} \) for any \( k \), then \( b' \) satisfies I - VI.

Proof: None of the \( t \)-labels or \( nt \)-labels change as a result of \( \pi \). This suffices to show that \( b' \) satisfies I - VI.

Lemma 7.3 Let \( b \) be a state of BCTSS satisfying I - VI. If \( (b, \text{UPDATE}_k((t_k, v_k), (nt_k, \text{val}_k)), b') \) is a step of BCTSS for any \( k \), then \( b' \) satisfies I - VI.

Proof: The proof is divided into a series of claims. By invariant I for state \( b \), \( b.t_{\max} \) and \( b.i_{\max} \) are defined. We split the argument into two cases: \( k = b.i_{\max} \) and \( k \neq b.i_{\max} \). Consider \( k = b.i_{\max} \) first.

Claim 7.3.4 If \( k = b.i_{\max} \), then \( b' \) satisfies I - VI.

Proof: By invariant II for state \( b \), \( b.t_k = b.nt_k \). Thus, none of the \( t \)-labels or \( nt \)-labels change for BCTSS. This suffices to show that \( b' \) satisfies I - VI.

So assume that \( k \neq b.i_{\max} \) for the remainder of the proof.

Claim 7.3.5 If \( k \neq b.i_{\max} \) then I is true in \( b' \).

Proof: Assume for a contradiction that \( \text{TOT}(b') = \text{false} \). Since \( \text{TOT}(b) = \text{true} \) and \( t_k \) is the only label that changes, the choice vector whose values are not totally ordered must include \( b'.t_k \). Now consider the same choice vector except that we substitute \( b.nt_k \) for \( b'.t_k \). Since \( b'.t_k = b'.nt_k \), this new choice vector’s values are also not totally ordered. Since none of the labels in this new choice vector change as a result of the action, the same choice vector must not have had its values totally ordered in state \( b \). However this contradicts the assumption that \( \text{TOT}(b) = \text{true} \).
Having proved invariant I we now know that $b', i_{max}$ and $b', t_{max}$ are defined. The proof for II - VI is subdivided into the following two cases: $b, nt_k \leq b, t_{max}$ and $b, t_{max} < b, nt_k$. Assume first that $b, nt_k \leq b, t_{max}$.

**Claim 7.3.6** If $k \neq b, i_{max}$ and $b, nt_k \leq b, t_{max}$ then $b', t_{max} = b, t_{max}$ and $b', i_{max} = b, i_{max}$ or $b', i_{max} = k$.

**Proof:** Let $z = b, i_{max}$, then $b, t_z = b, t_{max}$ and $z \neq k$. We show first that $b', t_i = b, t_z$ for all $i$. First consider $i \neq k$. Since $t_k$ is the only label that changes, $b', t_i = b, t_i$. Therefore, the fact that $b, t_i \leq b, t_z$ implies that $b', t_i \leq b, t_z$. Now let $i = k$. As a result of the action, $b', t_i = b, t_i$. By assumption $b, nt_i \leq b, t_z$, so $b', t_i \leq b, t_z$. Since $z \neq k$, $t_z$ does not change, so we can conclude that $b', nt_i \leq b', t_z$ for all $i$. This implies that $b', t_i = b', t_{max}$. The following identity now establishes the first part of the claim: $b, t_{max} = b, t_z = b', t_z = b', t_{max}$.

Let $S = \{i | b, t_i = b, t_{max}\}$ and $S' = \{i | b', t_i = b', t_{max}\}$ Then, $b, i_{max} = \text{MAX}(S)$ and $b', i_{max} = \text{MAX}(S')$. Since $t_k$ is the only label that changes and $b', t_{max} = b, t_{max}$, $S' = S$ or $S' = S - \{k\}$ or $S' = S \cup \{k\}$. When $S' = S$ then $\text{MAX}(S') = \text{MAX}(S)$. Let $z = b, i_{max}$. Since $k \neq b, i_{max}$, the definition of $b, i_{max}$ shows that $z \in S$ and $k \leq z$ when $k \in S$. Consequently, when $S' = S - \{k\}$ then $\text{MAX}(S') = \text{MAX}(S)$. Finally, when $S' = S \cup \{k\}$ then $\text{MAX}(S') = \text{MAX}(S)$ or $\text{MAX}(S') = k$. This shows that $b', i_{max} = b, i_{max}$ or $b', i_{max} = k$.

**Claim 7.3.7** If $k \neq b, i_{max}$ and $b, nt_k \leq b, t_{max}$ then $\text{NUM}(b', t_{max}, h) \geq \text{NUM}(b, t_{max}, h)$ and $\text{NUM}(b', t_{max}, h) \geq \text{NUM}(b, t_{max}, h)$ for all $i$ and $h$.

**Proof:** The Claim follows immediately if we show that $\text{AGREE}(b', t_{max}, h) \supseteq \text{AGREE}(b, t_{max}, h)$. Suppose $i \in \text{AGREE}(b, t_{max}, h)$. If $i \neq k$, then since $t_i$ does not change and, by Claim 7.3.6, $t_{max}$ does not change, $i \in \text{AGREE}(b', t_{max}, h)$. Now consider $i = k$. By definition of $\text{AGREE}$, $b, t_i \supseteq b, t_{max}$ and $b, nt_i \subsetneq b, t_{max}$. Since $b, nt_i \subsetneq b, t_{max}$, $b', t_i = b, t_{max}$ for state $b$ implies that $b, nt_i \supseteq b', t_{max}$. As a result of the action $b', t_i = b, nt_i$, so $b', t_i \supseteq b, t_{max}$. This fact along with the fact that $t_{max}$ does not change implies that $i \in \text{AGREE}(b', t_{max}, h)$.

**Claim 7.3.8** If $k \neq b, i_{max}$ and $b, nt_k \leq b, t_{max}$ then $b'$ satisfies II - VI.

**Proof:** We proceed with a case analysis. Consider any $i \in \{1 \ldots n\}$ and $h \in \{1 \ldots n - 1\}$.

II: Suppose $i = b', i_{max}$. By Lemma 7.3.6, $i = k$ or $i = b, i_{max}$. First consider $i = k$. As a direct consequence of the action, $b', t_i = b, nt_i$. Now consider $i = b', i_{max}$ where $i \neq k$. In this case II holds for $b'$ since $t_i$ and $nt_i$ do not change, and II holds for $b$.

III: III holds for $b'$ since $t_{max}$ and $nt_i$ do not change, and III holds for $b$.

IV: First consider $i = k$. As a consequence of the action $b', t_i = b, nt_i$. Hence, $b', t_i \supseteq b', t_{max}$ implies that $b, nt_i \supseteq b', t_{max}$ for all $h$. Now consider $i \neq k$. Since IV holds in state $b$, and $t_{max}$, $t_i$ and $nt_i$ do not change, IV holds for state $b'$.
V: First consider \( i = k \). \( b'.nt_i \in \text{cycle}(b'.t_{\text{max}}, h) \) and the definition of cycle imply that \( b'.nt_i \underset{h-1}{\leftrightarrow} b'.t_{\text{max}} \). As a consequence of the action, \( b'.ti_i = b'.nt_i \). Hence, \( b'.ti_i \underset{h-1}{\leftrightarrow} b'.t_{\text{max}} \). Now consider \( i \neq k \). In this case V is true in \( b' \) since \( ti_i, nt_i \), and \( t_{\text{max}} \) do not change and V is true in \( b \).

VI: Since \( nt_i \) and \( t_{\text{max}} \) do not change, \( b'.nt_i = \text{nextlabel}(b'.t_{\text{max}}, h) \) implies that \( b.nt_i = \text{nextlabel}(b.t_{\text{max}}, h) \), and \( b'.t_{\text{max}}[h] \neq 1 \) implies that \( b.t_{\text{max}}[h] \neq 1 \). By Claim 7.3.7, \( \text{num}(b'.t_{\text{max}}, h) \geq \text{num}(b.t_{\text{max}}, h) \) and \( \text{num}_i(b'.t_{\text{max}}, h) \geq \text{num}_i(b.t_{\text{max}}, h) \). Hence, VI holds for state \( b' \) since it holds for state \( b \).

Claim 7.3.8 shows that II - VI hold when \( b.nt_k \preceq b.t_{\text{max}} \). For the remainder of the proof assume that \( b.t_{\text{max}} \preceq b.nt_k \).

Claim 7.3.9 If \( k \neq b.i_{\text{max}} \) and \( b.t_{\text{max}} \preceq b.nt_k \) then \( b'.t_{\text{max}} = b'.t_k \) and \( b'.i_{\text{max}} = k \).

Proof: We proceed by showing that \( b'.ti_i \preceq b'.t_k \) for all \( i \neq k \). From the definition of \( t_{\text{max}} \) and the assumption that \( b.t_{\text{max}} \preceq b.nt_k \), we know that \( b.t_i \preceq b.t_{\text{max}} \preceq b.nt_k \). Let \( z = b.t_{\text{max}} \) then \( b.t_i = b.t_{\text{max}} \) and \( z \neq k \). Since \( k \neq z, k \neq i \), and \( b.t_z = b.t_{\text{max}} \), there exists a choice vector that includes the values \( b.t_i, b.t_{\text{max}} \), and \( b.nt_k \). Since \( \text{tot}(b) = \text{true} \), the values in this choice vector are totally ordered. Hence, \( b.t_i \preceq b.t_{\text{max}} \preceq b.nt_k \) implies that \( b.t_i \preceq b.nt_k \). As a result of the action \( b.nt_k = b'.t_k \) and \( t_i \) does not change. Therefore, \( b.t_i \preceq b.nt_k \) implies that \( b'.ti_i \preceq b'.t_k \). Hence \( b'.t_{\text{max}} = b'.t_k \). Since \( k \) is the only process index for which \( b'.t_{\text{max}} = b'.t_k \), \( b'.i_{\text{max}} = k \).

The following Claim lists some of the properties of \( b'.t_{\text{max}} \).

Claim 7.3.10 If \( k \neq b.i_{\text{max}} \) and \( b.t_{\text{max}} \preceq b.nt_k \) then there exists \( h' \in \{1 \ldots n-1\} \) such that:

1. \( b'.t_{\text{max}} = b'.t_k = b'.nt_k = b.nt_k = \text{nextlabel}(b.t_{\text{max}}, h') \).
2. \( b.t_{\text{max}}[h] = 1 \) for all \( h > h' \).
3. For all \( i, b'.nt_i \equiv b'.t_{\text{max}} \) implies that \( b'.nt_i = b'.t_{\text{max}} \).
4. There exists no \( i \neq k \) such that \( b'.ti_i \equiv b'.t_{\text{max}} \).
5. \( \text{num}(b'.t_{\text{max}}, h) \geq \text{num}(b.t_{\text{max}}, h) \) and \( \text{num}_i(b'.t_{\text{max}}, h) \geq \text{num}_i(b.t_{\text{max}}, h) \) for all \( i \) and all \( h < h' \).

Proof: By invariant III for state \( b \) and the assumption that \( b.t_{\text{max}} \preceq b.nt_k \), we conclude that \( b.nt_k = \text{nextlabel}(b.t_{\text{max}}, h') \) for \( h' \in \{1 \ldots n-1\} \). Fix \( h' \).

1. By Claim 7.3.9 \( b'.t_{\text{max}} = b'.t_k \). The fact that \( b'.t_k = b'.nt_k = b.nt_k \) is a direct consequence of the action \( \text{update}_k((t_k, e_k),(nt_k, val_k)) \). Finally, we have already shown that \( b.nt_k = \text{nextlabel}(b.t_{\text{max}}, h') \).
2. This follows directly from the definition of \text{nextlabel}.
3. Suppose that \( b'.nt_i \equiv b'.t_{\text{max}} \). First consider \( i \neq k \). The fact that \( nt_i \) does not change and part 1 of the claim show that \( b.nt_i = b'.nt_i \equiv b'.t_{\text{max}} = \text{nextlabel}(b.t_{\text{max}}, h') \). Consequently,
$ b \cdot t_i \xrightarrow{h'} = \text{nextlabel}(b \cdot t_{\text{max}}, h')$. Now the definition of \text{nextlabel} implies that $b \cdot t_i \xrightarrow{h' - 1} b \cdot t_{\text{max}}$ and $b \cdot t_i[h'] = \text{next}(b \cdot t_{\text{max}}[h'])$. Thus $b \cdot t_{\text{max}} \prec b \cdot t_i$. Now III for state $b$ implies that $b \cdot t_i \xrightarrow{h'} = \text{nextlabel}(b \cdot t_{\text{max}}, h')$ for some $h \in \{1 \ldots n - 1\}$. Since $b \cdot t_i[h'] = \text{next}(b \cdot t_{\text{max}}[h'])$, $h = h'$. Hence, $b' \cdot t_i = b \cdot t_i \xrightarrow{h'} = \text{nextlabel}(b \cdot t_{\text{max}}, h') = b' \cdot t_{\text{max}}$. Now consider $i = k$. In this case $b' \cdot t_{\text{max}} = b' \cdot t_k$ by part 1 of the claim.

4: We proceed by contradiction. Assume that there exists $i \neq k$ such that $b' \cdot t_i \xrightarrow{h'} = b' \cdot t_{\text{max}}$. Since $t_i$ does not change as a result of the action, $b \cdot t_i = b' \cdot t_i \xrightarrow{h'} = b' \cdot t_{\text{max}} = \text{nextlabel}(b \cdot t_{\text{max}}, h')$. Consequently, $b \cdot t_i \xrightarrow{h'} = \text{nextlabel}(b \cdot t_{\text{max}}, h')$. Now the definition of \text{nextlabel} implies that $b \cdot t_i \xrightarrow{h - 1} b \cdot t_{\text{max}}$ and $b \cdot t_i[h'] = \text{next}(b \cdot t_{\text{max}}[h'])$. Thus $b \cdot t_{\text{max}} \prec b \cdot t_i$. This contradicts the definition of $b \cdot t_{\text{max}}$.

5: Let $h < h'$. Part 5 of the Claim follows immediately if we show that $\text{agree}(b' \cdot t_{\text{max}}, h) \supseteq \text{agree}(b \cdot t_{\text{max}}, h)$. Suppose $i \in \text{agree}(b \cdot t_{\text{max}}, h)$. If $i \neq k$, then $t_i$ does not change. By part 1 of claim and the definition of \text{nextlabel}, $b' \cdot t_{\text{max}} \xrightarrow{h} b \cdot t_{\text{max}}$. Now the definition of \text{agree} implies that $i \in \text{agree}(b' \cdot t_{\text{max}}, h)$. Now consider $i = k$. Part 1 of the claim shows that $b' \cdot t_i = b' \cdot t_{\text{max}}$. Hence $i \in \text{agree}(b' \cdot t_{\text{max}}, h)$.

The remainder of the proof is structured as a series of claims, one for each of the five remaining invariants. Fix $h'$ to be the $h'$ defined by Claim 7.3.10. Parts 1-5 of Claim 7.3.10 will be used throughout the remaining claims.

Claim 7.3.11 If $k \neq b \cdot i_{\text{max}}$ and $b \cdot t_{\text{max}} < b \cdot t_k$ then II is true in $b'$.

**Proof:** By Claim 7.3.9 $b' \cdot i_{\text{max}} = k$. Part 1 of Claim 7.3.10 shows that $b' \cdot t_k = b' \cdot t_k$.

Claim 7.3.12 If $k \neq b \cdot i_{\text{max}}$ and $b \cdot t_{\text{max}} < b \cdot t_k$ then III is true in $b'$.

**Proof:** Consider any $i$ such that $b' \cdot t_{\text{max}} \prec b' \cdot t_i$. By part 1 of Claim 7.3.10, $b' \cdot t_{\text{max}} = b' \cdot t_k$ so $b' \cdot t_{\text{max}} \prec b' \cdot t_i$ implies that $i \neq k$. Furthermore, $t_i$ does not change as a result of the action and part 1 of Claim 7.3.10 shows that $b' \cdot t_{\text{max}} = b \cdot t_k$. Hence $b' \cdot t_{\text{max}} \prec b' \cdot t_i$ implies that $b \cdot t_k \prec b \cdot t_i$. By assumption $b \cdot t_{\text{max}} \prec b \cdot t_k$, so $b \cdot t_{\text{max}} \prec b \cdot t_k \prec b \cdot t_i$. Now consider two cases, $i = b \cdot i_{\text{max}}$ and $i \neq b \cdot i_{\text{max}}$. When $i = b \cdot i_{\text{max}}$, invariant II shows that $b \cdot t_{\text{max}} = b \cdot t_i$. This implies that $b \cdot t_i < b \cdot t_k < b \cdot t_i$, which is impossible by Lemma 4.1. Therefore, it must be that $i \neq b \cdot i_{\text{max}}$. Since $b \cdot i_{\text{max}} \neq i$ and $b \cdot i_{\text{max}} \neq k$ there must exist a choice vector that includes the values $b \cdot t_{\text{max}}, b \cdot t_i,$ and $b \cdot t_k$. Since $\text{tot}(b) = \text{true}$, the values in this choice vector are totally ordered. Hence, $b \cdot t_{\text{max}} \prec b \cdot t_k \prec b \cdot t_i$ implies that $b \cdot t_{\text{max}} \prec b \cdot t_i$. Now III for state $b$ and the fact that $nt_k$ does not change show that $b' \cdot nt_i = \text{nextlabel}(b' \cdot t_{\text{max}}, h)$ for some $h \in \{1 \ldots n - 1\}$. Since $b' \cdot nt_i = \text{nextlabel}(b' \cdot t_{\text{max}}, h)$, $b' \cdot t_{\text{max}} = \text{nextlabel}(b' \cdot t_{\text{max}}, h')$, and $b' \cdot t_{\text{max}} \prec b' \cdot t_i$, it must be that $h < h'$. Hence $b' \cdot t_i = \text{nextlabel}(b' \cdot t_{\text{max}}, h)$, which directly implies that I holds for state $b'$.

Claim 7.3.13 If $k \neq b \cdot i_{\text{max}}$ and $b \cdot t_{\text{max}} < b \cdot t_k$ then IV is true in $b'$.

**Proof:** Let $b' \cdot t_i \prec b' \cdot t_{\text{max}}$. First consider $i = k$. By part 1 of Lemma 7.3.10, $b' \cdot t_k = b' \cdot t_{\text{max}}$, which directly implies IV. Now consider $i \neq k$ and any $h$:
$h < h'$: Part 1 of Claim 7.3.10 and the definition of NEXTLABEL show that $b',t_{max} \overset{h}{=} b,t_{max}$ when $h < h'$. Now consider two cases: $b,n_i \preceq b,t_{max}$ and $b,n_i \not\preceq b,t_{max}$. When $b,n_i \preceq b,t_{max}$, IV for state $b$ shows that $b,t_i \overset{h}{=} b,t_{max}$ implies that $b,n_i \overset{h}{=} b,t_{max}$. Now IV is true in $b'$ since $t_i$ and $n_i$ do not change and $b',t_{max} \overset{h}{=} b,t_{max}$. Now consider the case $b,n_i \not\preceq b,t_{max}$. By Lemma 4.1, $b,t_{max} \prec b,n_i$. Now III for state $b$ shows that $b,n_i = \text{NEXTLABEL}(b,t_{max},h_i)$ for some $h_i \in \{1...n-1\}$. Furthermore, since $n_i$ does not change, the assumption that $b',n_i \preceq b',t_{max}$ implies that $b,n_i \overset{h}{=} b,t_{max}$. Finally, part 1 of Claim 7.3.10 shows that $b',t_{max} = \text{NEXTLABEL}(b,t_{max},h')$. Using these facts and the definition of NEXTLABEL we can conclude that $h_i > h'$. Therefore, $b,n_i = b',t_{max}$. Since $n_i$ does not change, this implies that $b',n_i \overset{h}{=} b',t_{max}$. This suffices to show that IV is true in $b'$.

$h \geq h'$: Part 4 of Claim 7.3.10 shows that $b',t_i \not\overset{h}{=} b',t_{max}$. Hence, IV is vacuously true in $b'$.

\[\square\]

**Claim 7.3.14** If $k \not= b,i_{max}$ and $b,t_{max} \prec b,n_k$ then $V$ is true in $b'$.

**Proof:** Suppose $b',n_i \in \text{CYCLE}(b',t_{max},h)$ for some $i$ and $h$. The definition of CYCLE implies that $b',n_i \overset{h-1}{=} b',t_{max}$. We consider two cases:

$h \leq h'$: First consider $i \not= k$. Part 1 of Claim 7.3.10 and the definition of NEXTLABEL show that $b',t_i \overset{h-1}{=} b',t_{max}$. Thus, $V$ is true in $b'$ since $t_i$ and $n_i$ do not change, CYCLE($b',t_{max},h$) depends only on $b',t_{max}[1...h-1]$, and $V$ is true in $b$. Now let $i = k$. In this case, part 1 of Claim 7.3.10 shows that $b',t_i = b',t_{max}$. This suffices to show $V$.

$h > h'$: Since $b',n_i \overset{h-1}{=} b',t_{max}$ and $h > h'$, it follows that $b',n_i \overset{h'}{=} b',t_{max}$. Thus part 3 of Claim 7.3.10 implies that $b',n_i = b',t_{max}$. By part 2 of Claim 7.3.10, $b',t_{max}[h] = 1$. Thus $b',n_i[h] = 1$, which implies that $b',n_i \not\in \text{CYCLE}(b',t_{max},h)$. This contradicts our original assumption that $b',n_i \in \text{CYCLE}(b',t_{max},h)$. Therefore, this case cannot arise.

\[\square\]

**Claim 7.3.15** If $k \not= b,i_{max}$ and $b,t_{max} \prec b,n_k$ then $Vib$ is true in $b'$.

**Proof:** Assume that $b',t_{max}[h] \not= 1$. We proceed with a case analysis:

$h < h'$: Part 1 of Claim 7.3.10 and the definition of NEXTLABEL show that $b',t_{max} \overset{h}{=} b,t_{max}$. Thus $b',t_{max}[h] \not= 1$ implies that $b,t_{max}[h] \not= 1$. Since $b,t_{max}[h] \not= 1$ and $Vib$ is true for $b$, $\text{NUM}(b,t_{max},h-1) \geq n - h + 1$. By part 5 of Claim 7.3.10 $\text{NUM}(b',t_{max},h-1) \geq \text{NUM}(b,t_{max},h-1)$. Thus, $\text{NUM}(b',t_{max},h-1) \geq n - h + 1$ which implies that $Vib$ is true for $b'$.

$h = h'$ and $b,t_{max}[h] \not= 1$: Since $b,t_{max}[h] \not= 1$ and $Vib$ is true for $b$, $\text{NUM}(b,t_{max},h-1) \geq n - h + 1$. By part 5 of Claim 7.3.10 $\text{NUM}(b',t_{max},h-1) \geq \text{NUM}(b,t_{max},h-1)$. Thus, $\text{NUM}(b',t_{max},h-1) \geq n - h + 1$ which implies that $Vib$ is true for $b'$.

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\[ h = h' \text{ and } b.t_{\max}[h] = 1: \text{ Part } 1 \text{ of Claim } 7.3.10 \text{ and the fact that } h' = h \text{ imply that } b.n_t = \text{nextlabel}(b.t_{\max}, h). \text{ Since } b.n_t = \text{nextlabel}(b.t_{\max}, h) \text{ and Vla is true for state } b, \text{ num}_{k}(b.t_{\max}, h-1) \geq n-h. \text{ By part } 5 \text{ of Claim } 7.3.10 \text{ num}_{k}(b'.t_{\max}, h-1) \geq \text{num}_{k}(b.t_{\max}, h-1). \text{ Thus, } \text{num}_{k}(b'.t_{\max}, h-1) \geq n-h. \text{ Since } b'.t_{\max} = b'.t_k, k \in \text{agree}(b'.t_{\max}, h). \text{ Therefore } \text{num}(b'.t_{\max}, h-1) > \text{num}(b'.t_{\max}, h-1) \geq n-h. \text{ Thus, } \text{num}(b'.t_{\max}, h-1) \geq n-h+1, \text{ which implies that Vlb is true for } b'.
\]

\[ h > h': \text{ Part } 2 \text{ of Claim } 7.3.10 \text{ and the fact that } h > h' \text{ imply that } b'.t_{\max}[h] = 1. \text{ This contradicts the assumption that } b'.t_{\max}[h] \neq 1. \text{ Therefore, this case cannot arise.} \]

**Claim 7.3.16** If \( k \neq b.i_{\max} \) and \( b.t_{\max} < b.n_t \) then Vla is true in \( b' \).

**Proof:** Let \( b'.n_t = \text{nextlabel}(b'.t_{\max}, h) \) for some \( h \) and \( i \). We proceed with a case analysis:

\[ h < h': \text{ Part } 1 \text{ of Claim } 7.3.10 \text{ and the definition of } \text{nextlabel} \text{ show that } b'.t_{\max} = b.t_{\max}. \text{ Now the fact that } n_t \text{ does not change and the fact that } b'.n_t = \text{nextlabel}(b'.t_{\max}, h) \text{ imply that } b.n_t = \text{nextlabel}(b.t_{\max}, h). \text{ Since } b.n_t = \text{nextlabel}(b.t_{\max}, h) \text{ and Vla is true in state } b, \text{ num}_{i}(b.t_{\max}, h-1) \geq n-h. \text{ Part } 5 \text{ of Claim } 7.3.10 \text{ shows that } \text{num}_{i}(b'.t_{\max}, h-1) \geq \text{num}_{i}(b.t_{\max}, h-1). \text{ Therefore, } \text{num}_{i}(b'.t_{\max}, h-1) \geq n-h \text{ which implies that Vla is true for } b'.
\]

\[ h = h'^2: \text{ Using part } 1 \text{ of Claim } 7.3.10 \text{ and the definition of } \text{nextlabel} \text{ we can conclude that } b'.t_{\max}[h] = \text{next}(b'.t_{\max}[h]). \text{ There exists no } z \in \mathcal{A} \text{ such that } \text{next}(z) = 1. \text{ Hence } b'.t_{\max}[h] \neq 1. \text{ Claim } 7.3.16 \text{ implies that Vlb holds for state } b'. \text{ Since } b'.t_{\max}[h] \neq 1, \text{ Vlb for state } b' \text{ implies that } \text{num}(b'.t_{\max}, h-1) \geq n-h+1. \text{ Thus } \text{num}_{i}(b'.t_{\max}, h-1) \geq n-h \text{ and Vla is true in state } b'.
\]

\[ h > h': \text{ The fact that } b'.n_t = \text{nextlabel}(b'.t_{\max}, h) \text{ and the definition of } \text{nextlabel} \text{ imply that } b'.n_t = b'.t_{\max}. \text{ Now part } 3 \text{ of Claim } 7.3.10 \text{ and the fact that } h > h' \text{ imply that } b'.n_t = b'.t_{\max}. \text{ Thus } b'.n_t \neq \text{nextlabel}(b'.t_{\max}, h) \text{ which contradicts our assumption that } b'.n_t = \text{nextlabel}(b'.t_{\max}, h). \text{ Therefore, this case cannot arise.} \]

We now complete the proof of the lemma. To show that \( b' \) satisfies I - VI we consider two cases: \( k = b.i_{\max} \) and \( k \neq b.i_{\max} \). Claim 7.3.4 shows that \( b' \) satisfies I - VI when \( k = b.i_{\max} \). When \( k \neq b.i_{\max} \) Claim 7.3.5 shows that invariant I holds in state \( b' \). The proof for invariants II - VI is subdivided into two cases: \( b.n_t \leq b.t_{\max} \) and \( b.t_{\max} < b.n_t \). Claim 7.3.8 shows that II - VI hold when \( b.n_t \leq b.t_{\max} \). Claim 7.3.11, Claim 7.3.12, Claim 7.3.13, Claim 7.3.14, Claim 7.3.15 and Claim 7.3.16 each consider one of the invariants to show that II - VI hold when \( b.t_{\max} < b.n_t \).
Lemma 7.4 Let \( b \) be a state of bctss that satisfies I - VI. If \( (b, \text{snap}_k(\bar{f}_k, \bar{e}_k), b') \) is a step of bctss for any \( k \), then \( b' \) satisfies I - VI.

Proof: Note that none of the \( t \)-labels or \( n_t \)-labels change when \( \text{op}_k = \text{scan}_k \). Therefore, assume that \( \text{op}_k = \text{label}_k \). The proof is divided into series of claims. First consider the case where \( k = b.i_{\max} \).

Claim 7.4.17 If \( k \neq b.i_{\max} \) then II - VI are true in \( b' \).

Proof: ViB holds in state \( b' \) since it holds in state \( b \) and no \( t \)-labels change. Now consider II - Vla. If \( i \neq k \), then the definition of \( \text{snap}_k(\bar{f}_k, \bar{e}_k) \) shows that neither \( t_i, n.t_i, t_{\max} \), nor \( \text{num}_i(t_{\max}, h) \) change. Therefore, II - Vla are true in state \( b' \) since II - Vla are true in state \( b \). So assume that \( i = k \). In this case \( b'.n.t_i = \text{nextlabel}(b.t_{\max}, h') \) and \( b'.t_{\max} < b'.n.t_i \). Consider II - Vla separately:

II: Since \( k \neq b.i_{\max} \), \( i \neq b.i_{\max} \). Furthermore, \( b.i_{\max} = b'.i_{\max} \) thus \( i \neq b'.i_{\max} \). Now II is vacuously true in state \( b' \).

III: Since \( b'.t_{\max} = b.t_{\max} \), and \( b'.n.t_i = \text{nextlabel}(b.t_{\max}, h'), b'.n.t_i = \text{nextlabel}(b'.t_{\max}, h') \).

IV: Since \( b'.t_{\max} = b.t_{\max} < b'.n.t_i \) IV is vacuously true in \( b' \).

V: Suppose that \( b'.n.t_i \in \text{cycle}(b'.t_{\max}, h) \) where \( h \in \{1 \ldots n - 1\} \). The definition of \text{cycle} now implies that \( b'.n.t_i[h] \in \{3,4,5\} \). Recall that \( b'.n.t_i = \text{nextlabel}(b.t_{\max}, h') \). The definition of \text{nextlabel} implies that \( b'.n.t_i[h'] = 1 \) for all \( h' > h \). Since \( b'.n.t_i[h] \in \{3,4,5\} \), we can conclude that \( h \leq h' \). We consider the two cases \( h = h' \) and \( h < h' \) separately.

First consider the case \( h = h' \). Since \( \text{next}(1) \notin \{3,4,5\} \), and \( \text{next}(b.t_{\max}[h]) = b'.n.t_i[h] \notin \{3,4,5\}, b.t_{\max}[h] \neq 1 \). Now ViB for state \( b \) shows that \( \text{num}(b.t_{\max}, h - 1) \geq n - h + 1 \). Furthermore, Claim 5.1.2 and the fact that \( i = k \) show that \( \text{num}_i(b.t_{\max}, h - 1) < n - h + 1 \).

Since \( \text{num}(b.t_{\max}, h - 1) \geq n - h + 1 \) and \( \text{num}_i(b.t_{\max}, h - 1) < n - h + 1, k \in \text{agree}(b.t_{\max}, h - 1) \). Thus \( b.t_i \overset{h-1}{=} b.t_{\max} \). Since \( t_i \) and \( t_{\max} \) do not change, \( b'.t_i \overset{h-1}{=} b'.t_{\max} \).

Now consider the case \( h < h' \). The fact that \( b'.n.t_i = \text{nextlabel}(b.t_{\max}, h') \) and the definition of \text{nextlabel} imply that \( b.t_{\max}[h] = b'.n.t_i[h] \). Therefore, \( b.t_{\max}[h] \neq 1 \) since \( b.t_{\max}[h] = b'.n.t_i[h] \in \{3,4,5\} \). Now ViB for state \( b \) shows that \( \text{num}(b.t_{\max}, h - 1) \geq n - h + 1 \). The definition of \text{nextlabel} and the fact that \( i = k \) show that \( \text{full}(h - 1) \) returns \text{false}, which implies that \( \text{num}_i(b.t_{\max}, h - 1) < n - h + 1 \). Since \( \text{num}(b.t_{\max}, h - 1) \geq n - h + 1 \) and \( \text{num}_i(b.t_{\max}, h - 1) < n - h + 1, i \in \text{agree}(b.t_{\max}, h - 1) \). Thus \( b.t_i \overset{h-1}{=} b.t_{\max} \). Since \( t_i \) and \( t_{\max} \) do not change, \( b'.t_i \overset{h-1}{=} b'.t_{\max} \).

Vla: Since \( b'.t_{\max} = b.t_{\max} \) and \( b'.n.t_i = \text{nextlabel}(b.t_{\max}, h') \), we conclude that

\[ b'.n.t_i = \text{nextlabel}(b'.t_{\max}, h') \]

Now, Claim 5.1.2 implies that \( \text{num}_i(b'.t_{\max}, h' - 1) = n - h' \).
We can now complete the proof of the lemma. Claim 5.1.1 shows that I - VI hold for $b'$ when $k = b_i^\max$. When $k \neq b_i^\max$, Claim 5.1.3 shows that I holds in $b'$ and Claim 7.4.17 shows that II - VI hold for $b'$.

8 Discussion and Future Work

All know applications of timestamp systems use tCTSS. This paper provides a bounded implementation of tCTSS, so the correctness proofs of the applications using timestamp systems can assume that they are using a tCTSS even though the actual implementation would make use of our bounded BCTSS. The time complexity of our BCTSS construction is simply the complexity of the underlying atomic snapshot implementation.

In recent years, much progress has been made in the area of automatic theorem provers. Large parts of our correctness proof, especially the proof for the invariants in Section 5 use an extensive, well structured case analysis. Each case is proved by a simple but tedious argument. Consequently, we view the correctness proof of our bounded timestamp algorithms as an ideal candidate with which to test the effectiveness of automatic theorem provers [27]. In testing a theorem prover on our algorithm we hope to determine whether or not I/O Automata proofs might in the future utilize theorem provers on a regular basis.

References


