

Automated Verification = Graphs, Automata, and Logic

—Invited Lecture—

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Abstract

In automated verification one uses algorithmic techniques to establish the correctness of the design with respect to a given property. Automated verification is based on a small number of key algorithmic ideas, tying together graph theory, automata theory, and logic. In this self-contained talk I will describe how this “holy trinity” gave rise to automated-verification tools.